PAPERS

Checking Experiments
Totally Preset Checking Experiments for Sequential Machines

G. K. Lin and P. R. Menon

G. V. Bochmann and M. Raynal

Spatial Planning: A Configuration Space Approach

T. Lozano-Pérez

Structured Specification of Communicating Systems

Distributed Systems

A Distributed Channel-Access Protocol for Fully-Connected Networks with Mobile Nodes

Y. I. Gold, W. R. Franta, and S. Moran

Minimum Mean Running Time Function Generation Using Read-Only Memory

G. H. Garcia and W. J. Kubitz

Language Oriented Architecture

Execution Architecture: The DELtran Experiment

M. J. Flynn and L. W. Hoevel

Multiprocessor Systems

The NYU Ultracomputer—Designing an MIMD Shared Memory Parallel Computer

A. Gottlieb, R. Grishman, C. P. Kruskal, K. P. McAuliffe, L. Rudolph, and M. Snir

Self-Testing Techniques

Exhaustive Generation of Bit Patterns with Applications to VLSI Self-Testing

Z. Barzilai, D. Coppersmith, and A. L. Rosenberg

CORRESPONDENCE

A Mesh-Connected Area-Time Optimal VLSI Multiplier of Large Integers

F. P. Preparata

Testing by Verifying Walsh Coefficients

A. K. Susskind

Addendum to "Optimum Golomb Rulers"

J. P. Robinson

The Parallel Solution of Triangular Systems of Equations

D. J. Evans and R. C. Dunbar

On the Microprocessor Solution of Ordinary Differential Equations Using Integer Arithmetic

A. R. Bazelow and J. Raamot