PAPERS

Boolean Algebra
Realization of Boolean Expressions by Atomic Digraphs............. R. E. Prather and H. T. Casstevens, II 681

Fault Simulation
Deductive Fault Simulation with Functional Blocks.................... P. R. Menon and S. G. Chappell 689

Fault Tolerant Memories
Erasure and Error Decoding for Semiconductor Memories................ C.-E. W. Sundberg 696

Memory Hierarchy Management

Network Design
Meet and Join Derivatives and Their Use in Switching Theory................ A. Thayse 713

Pattern Recognition
A Multifont Word Recognition System for Postal Address Reading............... J. Schürmann 721

System Architecture
System Functions for an Optical/Digital Processor...................... D. Casasent and P. D. Rapp 732

CORRESPONDENCE
Texture Characterization and Texture-Based Image Partitioning Using Two-Dimensional Linear Estimation Techniques.......................... K. Deguchi and I. Morishita 739
Irrelevant Features in Pattern Recognition............................................. M. Ben-Bassat 746
Realization of Minimum Circuits with Two-Input Conservative Logic Elements........ T. Sasao and K. Kinoshita 749
A Synthesizing Method for Large Parallel Counters with a Network of Smaller Ones................................................................. H. Kobayashi and H. Ohara 753
On the Fundamental Structure of Galois Switching Functions................ B. Benjauthrit and I. S. Reed 757
Fast Multipliers for Number Theoretic Transforms.............................. H. J. Nussbaumer 764
Some Codes of Rahman and Blake for Computer Applications.............. V. K. Bhargava 765
On the Mean Accuracy of Hierarchical Classifiers................................. A. V. Kulkarni 771
Detection of Ellipses by a Modified Hough Transformation.................. S. Tsuji and F. Matsumoto 777
On Universal Single Transition Time Asynchronous State Assignments............ T. Nanya and Y. Tohma 781