Welcome to the International Workshop on Performance Modeling and Analysis of High Speed Interconnects (MAHSI2009)! The workshop is held in conjunction with the IEEE International Conference on Scalable Computing and Communications (ScalCom-09), September 25th and 27th, 2009 in Dalian, China.

The performance of the interconnection networks employed in computers and communication systems has often been a crucial determining factor of the overall application and system performance. This will be the case as applications become more complex with high computation and communication requirements. As a consequence, there has been an ever increasing demand for developing tools and techniques which can assist in the understanding of the performance behavior and evaluation of competing network design alternatives.

This workshop brings together scientists, engineers, practitioners, and computer users to share and exchange their experiences, discuss challenges, and report state-of-the-art and in-progress research on all aspects of performance modeling and evaluation of high-speed interconnects. All the received submissions were peer-reviewed and 11 papers have been selected for presentation at the workshop. The selected papers tackle different aspect of performance modeling and analysis of communication and computer systems ranging from parallel processing, network-on-chip, to wireless mobile networks.

We would like to thank the authors who have submitted their papers to MAHSI’2009 and congratulate the authors of the accepted papers. Our sincere thanks go to the program committee members and to the ScalCom Conference Chairs for their cooperation and efforts in finalizing the technical program of the workshop.

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