MSC – Microprocessor Standards Committee

Status Report

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Microprocessor Standards Committee
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MSC Activity–WGs

- The editorial for the P1823 document, Standard for Universal Power Adapter for Mobile Devices, is complete, and the document is expected to be published in May.
- The P1788 document, Standard for Interval Arithmetic, has passed Sponsor Ballot and is on REVCOM's June 5 agenda.
- The P1722 document, Standard for a Transport Protocol for Time Sensitive Applications in a Bridged Local Area Network, was under sponsor ballot until May 14. As of May 5, there was a 71% response rate, with 91% approval. An in-person meeting of the working group is scheduled for August 10 and 11.
- The P1394 Revision document, Standard for a High-Performance Serial Bus, as well as P1722.1 (Corrigendum) and P1722 Revision, are expected to go to sponsor ballot some time in 2015.
- A study group for standardizing memory, storage, and processor interconnection was formed.
The MSC chair and co-chair reviewed standards that will be expiring in 2018 and identified:

- 754-2008 (Floating point arithmetic),
- 1284/1 (transport independent printer/system interface),
- 1362 (public key cryptographic standards),
- 1596 (scalable coherent interface), and
- 1620 (test methods for organic transistors)

as worthy of revision PARs.
MSC (cont.)

- We are taking steps to reconstitute working groups.
- We are in the process of identifying and remedying obsolete IEEE web pages related to the MSC.
We thank Jonathan Goldberg and IEEE Staff, for the work and support to the MSC. We take this opportunity to recognize Don Messina, in the editorial office, who is retiring July 1.