
These colleagues were chosen through an extensive selection process which was approved by the IEEE Computer Society. I would like to welcome them to the board and I am sure their presence will contribute to the continued success of TC.

Also, few of our Associate Editors concluded their tenure: Elisardo Antelo, Cristiana Bolchini, George Constantinides, Kanad Ghose, Dimitris Gizopoulos, John Chi-Shing Lui, Cecilia Metra, Walid Najjar, Sotiris Nikoletseas, Sang Hyuk Son, and Spyros Tragoudas. I would like to express our thanks to these colleagues for all their professionalism and hard work and wish them well with their future endeavors.

Please feel free to send me your suggestions and recommendations. I welcome your ideas and suggestions on ways by which we can improve TC. I also look forward to receiving your technical submissions.

Albert Y. Zomaya
Editor-in-Chief
Habib M. Ammari is an associate professor in the Department of Computer and Information Science, College of Engineering and Computer Science, University of Michigan-Dearborn, and the founding director of Wireless Sensor and Mobile Ad-hoc Networks (WiSeMAN) Research Lab at the University of Michigan-Dearborn since September, 2011. Prior to that, he was on the faculty of the Department of Computer Science, Hofstra University from September, 2008 – August, 2011. He obtained his second PhD degree in computer science and engineering from the University of Texas at Arlington in May, 2008, and his second Master’s degree in computer science from Southern Methodist University in December, 2004. Also, he obtained his first PhD (highly honorable with praise) and Master’s degrees in computer science from the Faculty of Sciences of Tunis in December, 1996 and July, 1992, respectively. His main research interests lie in the areas of wireless sensor and mobile ad hoc networking, multi-hop mobile wireless Internet architectures and protocols, and cyber physical systems. In particular, he is interested in coverage, connectivity, energy-efficient data routing and information dissemination, fault tolerance, and security in wireless sensor networks, and the interconnection between wireless sensor networks, mobile ad hoc networks, and the global IP Internet. He has a strong publication record in top-quality journals, such as IEEE TPDS, IEEE TC, ACM TAAS, Elsevier COMNET, Elsevier PMC, Elsevier JPDC, and high-quality conferences, such as IEEE SECON, IEEE ICDCS, EWSN, and ICDCN. He published his first Springer book, titled “Challenges and Opportunities of Connected k-Covered Wireless Sensor Networks: From Sensor Deployment to Data Gathering”, in August, 2009. He received several prestigious awards, including the Certificate of Appreciation Award at the 17th ACM MobiCom in September, 2011, the Outstanding Leadership Award at the 20th IEEE ICCCN in August, 2011, the Best Symposium Award at the 7th IEEE IWCMC in July, 2011, the Lawrence A. Stessin Prize for Outstanding Scholarly Publication from Hofstra University in May, 2010, the Faculty Research and Development Grant Award from Hofstra College of Liberal Arts and Sciences in May 2009, the Best Paper Award at the 5th EWSN in February, 2008, and the Best Paper Award at the Google PhD Forum – 6th IEEE PerCom in March, 2008. Also, he was an ACM Student Research Competition (ACM SRC) nominee at ACM MobiCom 2005. He is the recipient of the Nortel Outstanding CSE Doctoral Dissertation Award in February 2009, and the John Steven Schuchman Award for 2006-2007 Outstanding Research by a PhD Student in February 2008. He serves as associate editor of several journals, including ACM TOSN, IEEE TC, Wiley WC, AHSWN, Wiley IJCS, and NPA. He is on the Editorial board of the International Journal of Mobile Communications and the International Journal on Advances in Networks and Services. Also, he is on the Editorial Review Board of the International Journal of Distributed Systems and Technologies. He serves as track cochair of IEEE ICCCN 2012 – SEP, Demo cochair of IEEE WoWMoM 2012, program cochair of CPNS 2012, program vice-chair of MUSIC 2012, workshop chair of WiMAN 2012, program vice-chair of IEEE/IFIP EUC 2011, workshop chair of WiMAN 2011, and symposium cochair of IWCMC 2011 Wireless Sensor Networks Symposium. Also, he served as workshop cochair of WiMAN 2010, symposium cochair of IWCMC 2010 Wireless Sensor Networks Symposium, program cochair of IQ2S 2009, and Workshop Cochair of WiMAN 2009. He has served as publicity cochair of numerous conferences, symposia, and workshops, including ACM MobiCom 2011. Also, he has served as a reviewer for several international journals, including IEEE TMC, IEEE TPDS, IEEE SMC, ACM TOSN, IEEE TVT, IEEE TWireless, IEEE CL, Springer’s MONE, Springer’s WINET, Elsevier’s ADHOC, Elsevier’s COMNET, AHSWN, IJSNet, Elsevier’s IPL, Elsevier’s COMCOM, Elsevier’s JPDC, Elsevier’s INS, IFCA, and Elsevier’s DKE, and as a technical program committee member of numerous IEEE and ACM conferences, symposia, and workshops, including IEEE Infocom, IEEE ICDCS, IEEE DCOSS, IEEE INSS, IEEE PerCom, IEEE GlobeCom, IEEE ICC, SSS, IEEE MASS, IEEE MSN, IEEE LCN, IEEE VTC, IEEE ICCCN, EWSN, ICDCN, and AdHocNets.

Jean-Luc Beuchat received his MSc and his PhD in computer science from the Swiss Federal Institute of Technology, Lausanne, in 1997 and 2001, respectively. Currently, he is an associate professor in the Graduate School of Systems and Information Engineering at the University of Tsukuba. His research interests are in the areas of cryptography, computer arithmetic, and computer architecture. He is the author of more than 30 publications in technical journals and conferences. His work has been recognized with two best paper awards at CHES, the world’s foremost conference in cryptographic hardware. He has served as a reviewer and program committee member for numerous conferences. He is a member of the IACR.
Krishnendu Chakrabarty received the B. Tech. degree from the Indian Institute of Technology, Kharagpur, in 1990, and the MSE and PhD degrees from the University of Michigan, Ann Arbor, in 1992 and 1995, respectively. He is now professor of electrical and computer engineering at Duke University. He is also a chair professor in software theory at Tsinghua University, Beijing, China, and a visiting chair professor of computer science and information engineering at National Cheng Kung University in Taiwan. Professor Chakrabarty is a recipient of the US National Science Foundation Early Faculty (CAREER) award, the Office of Naval Research Young Investigator award, the Humboldt Research Fellowship from the Alexander von Humboldt Foundation, Germany, and several best papers awards at IEEE conferences. Professor Chakrabarty’s current research projects include: testing and design-for-testability of integrated circuits; digital microfluidics, biochips, and cyberphysical systems; optimization of digital print and enterprise systems. In the recent past, he has also led projects on wireless sensor networks, embedded systems, and real-time operating systems. He has authored 12 books on these topics, published over 400 papers in journals and refereed conference proceedings, and given over 180 invited, keynote, and plenary talks. Professor Chakrabarty is a fellow of IEEE, a golden core member of the IEEE Computer Society, and a distinguished engineer of ACM. He was a 2009 invitational fellow of the Japan Society for the Promotion of Science (JSPS). He is a recipient of the 2008 Duke University Graduate School Dean’s Award for excellence in mentoring, and the 2010 Capers and Marion McDonald Award for Excellence in Mentoring and Advising, Pratt School of Engineering, Duke University. He served as a distinguished visitor of the IEEE Computer Society during 2005-2007, and as a distinguished lecturer of the IEEE Circuits and Systems Society during 2006-2007. Currently he serves as an ACM distinguished speaker, a distinguished visitor of the IEEE Computer Society for 2010-2012, and a distinguished lecturer of the IEEE Circuits and Systems Society (2012-2013). Professor Chakrabarty is the Editor-in-Chief of IEEE Design & Test of Computers and ACM Journal on Emerging Technologies in Computing Systems. He is also an associate editor of IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on Circuits and Systems II, and IEEE Transactions on Biomedical Circuits and Systems. He serves as an Editor of the Journal of Electronic Testing: Theory and Applications (JETTA). In the recent past, he has served as associate editor of IEEE Transactions on VLSI Systems and IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, and IEEE Transactions on Circuits and Systems I.

Hsien-Hsin S. Lee is an associate professor in the School of Electrical and Computer Engineering at the Georgia Institute of Technology, Atlanta. He received his BS degree in electrical engineering from National Tsinghua University in Taiwan, and his MS and PhD degree in computer science and engineering from the University of Michigan, Ann Arbor. His main research interests include computer architecture, energy-efficient computing, datacenter computing, cyber security, and the emerging 3-D IC technology. Prior to joining Georgia Tech in 2002, he spent six years as a senior processor architect and a researcher at Intel Corporation and one year at Agere Systems as an architecture manager for their StarCore DSP. Dr. Lee received the Horace H. Rackham Distinguished Dissertation Award from the University of Michigan, an NSF CAREER Award, an US Department of Energy Early CAREER Award, the Georgia Tech ECE Outstanding Jr. Faculty Award, and an IBM Faculty Award. He coauthored four papers that won the Best Paper Award in MICRO-33, CASES-2004, IBM PAC2, and ANCS-11, one paper selected in the IEEE MICRO Top Picks of Computer Architecture Conferences in 2010, and another three nominated for the Best Paper Award including MIT HPEC-07, FPL-07, and ICCAD-09. He has served in the editorial boards and was a guest editor of several international journals including ACM Transactions on Architecture and Code Optimization and the IEEE Transactions on Computer-Aided Design for Integrated Circuits and Systems. He was the general chair of the IEEE International Symposium on Workload Characterization in 2010. He holds four US patents and is a senior member of both the ACM and the IEEE.

Keqiu Li is currently a professor at the School of Computer Science and Technology, Dalian University of Technology, China. He got his bachelor and masters degree both from Dalian University of Technology, China in 1994 and 1997, and his doctor degree from Japan Advanced Institute of Science and Technology, Japan in 2005. Keqiu Li was a research fellow at the University of Tokyo, Japan from October 2005 to September 2007. Keqiu Li’s research interests include content distribution networks, cloud computing, and multimedia retrieval. Keqiu Li has published more than 100 technical papers in international journals and conferences, such as IEEE TPDS, IEEE TKDE, ACM TOIT, and ACM TOMAPP. He is on the committee board for several international and national journals including IEEE TPDS and serves as organization chair, program chair, publication chair, and program committee member for a couple of international conferences. Keqiu Li is a member of the IEEE.
Ahmed Louri received the MS and PhD degrees in computer engineering from the University of Southern California, Los Angeles in 1984 and 1988 respectively. He joined the University of Arizona in 1988 where he is currently a professor of electrical and computer engineering and the director of the High Performance Computing Architectures and Technologies Laboratory (http://www.ece.arizona.edu/~hpcat). Dr. Louri chaired the computer engineering program of the Electrical and Computer Engineering Department from 2000 to 2006. He previously held visiting scientist positions at the Communications Research Laboratory (Tokyo, Japan), Laboratoire d’Informatique du Parallélisme (LIP) (Lyon France), University of Tsukuba (Tsukuba, Japan), Universite de Paul Sabatier (Toulouse, France). His research interests include computer architecture, parallel processing, interconnection networks, optical interconnects for parallel computing systems, dynamic bandwidth allocation and dynamic power management techniques, reconfigurable computing systems, fault-tolerant multiprocessors, area and energy efficient on-chip networks for multi-core architectures, reliable and fault-tolerant network-on-chips, emerging technologies for interconnection networks, photonic on-chip networks for multi-core architectures and Chip Multiprocessors (CMPs). He has published more than 125 journal articles and conference papers in these areas, and holds several US patents. His research has been sponsored by NSF, DOE, AFOSR, and a number of industrial organizations. Dr. Louri is the recipient of the US National Science Foundation Research Initiation Award (1989), the Best article Award from IEEE Micro (1991), the Advanced Telecommunications Organization of Japan Fellowship, the Centre Nationale de Recherche Scientifique (CNRS), France, Fellowship, and the Japan Society for the Promotion of Science Fellowship, and several teaching awards. Dr. Louri was instrumental in bringing optical interconnects into mainstream research in interconnection networks and bridging the gap between computer architecture and optics research communities. He served as a general chair for the 13th Annual Symposium of the High Performance Computer Architecture (HPCA-13), Phoenix, Arizona, 2007, the general cochair of the Second Workshop on Optics in Communications and Computer Sciences (WOCCS-99), Toulouse France, 1999, and the general chair for the Workshop on Optics in High-Performance Computing Systems, Lyon France 1996. He has served as a technical committee member for several international conferences including, Optical Society of America, meetings on Optics in Computing, the IEEE/OSA conference on Massively Parallel Processing using Optical Interconnects (MPPOI), the International conference on Parallel and Distributed computing and Systems (PDCS), the International Conference on Computing and Information (ICCI), the International Conference on Parallel Processing (ICPP), the International Symposium on High Performance Computer Architecture (HPCA), the ACM/IEEE Symposium on Architectures for Networking and Communication Systems (ANCS), The IEEE/ACM international Symposium on Microarchitecture (IEEE Micro), the IEEE/ACM International Symposium on Networks-on-Chip (NOCS), and the International Symposium on Computer Architecture and High Performance Computing (SBAC-PAD). He served as associate editor of Cluster Computing: The Journal of Networks, Software Tools, and Applications, Baltzer Science Publishers, and guest editor of the Journal of Parallel and Distributed Computing (JPDS), Academic Press. He has also served on many NSF, DoE and DoD panels. Dr. Louri is a Fellow of IEEE, a regular member of OSA, a member of the International Society for Optical Engineering working Group on Optical Computing, a member of the IEEE Society Technical Committee on Computer Architecture and a member of the IEEE Technical Committee on Parallel Processing. Professor Ahmed Louri is currently a program director at the US National Science Foundation in Arlington, Virginia where he is managing the National Computer Architecture Program in the Software/Hardware Cluster (SHF), Division of the Computing and Communication Foundations (CCF), Directorate for Computer & information Science & Engineering (CISE).

Binoy Ravindran is an associate professor in the Department of Electrical and Computer Engineering at Virginia Tech. He received his PhD degree in computer science from the University of Texas at Arlington. His research interests include real-time, embedded, and networked systems, with a particular focus on resource management at various levels of abstraction. His research problem spaces have included scheduling (single processors, multiprocessors, distributed systems); concurrency and synchronization; memory management and garbage collection; fault-tolerance; power management, networking, and OS and middleware. He and his students have published more than 180 papers on these topics. Many of his group’s results have been transitioned to US Department of Defense programs. Dr. Ravindran’s 2006 ACM Design, Automation, and Test in Europe (DATE) conference paper was selected as one of the most influential papers of 10 Years of DATE. Major sponsors of Dr. Ravindran’s research include US National Science Foundation, US Office of Naval Research, US Naval Surface Warfare Center, The MITRE Corporation, DARPA, and NASA. Dr. Ravindran was a keynote speaker at the 1999 ARTES (Swedish National Strategic Research Initiative in Real-Time Systems) Real-Time Week, Sweden, and the 2007 International Conference on Embedded Software and Systems. He has served on the program committees of major conferences in real-time/embedded/distributed systems (e.g., RTSS, RTAS, DATE, SPAA, OPODIS, SSS), was the program cochair of the 1999 IEEE International Workshop on Parallel and Distributed Real-Time Systems, the 2008 IEEE International Workshop on Object-oriented Real-time Dependable Systems, and the track cochair for the 2009 ACM Symposium on Applied Computing’s Track on Real-Time Systems. Dr. Ravindran is an US Office of Naval Research Distinguished Faculty Fellow, is an associate editor of ACM Transactions on Embedded Computing Systems, was a coguest-editor for IEEE Transactions on Computers, and was an IEEE Distinguished Visitor. Dr. Ravindran currently serves as an ACM Distinguished Speaker.
Patrick Schaumont is an associate professor in computer engineering at Virginia Tech. He received the PhD degree in electrical engineering from UCLA (2004). He has been a researcher at the Inter-University Micro-Electronics Center (IMEC) in Belgium from 1992 to 2001. His research interests include design of, and design methodologies for, secure embedded systems. He has served as guest editor for IEEE Design and Test Magazine, ACM Transactions on Reconfigurable Technology and Systems, and IEEE Transactions on Computer-Aided Design of Integrated Circuits. He is an associate editor for IACR Journal of Cryptographic Engineering and ACM Transactions on Design Automation of Electronic Systems. He is serving as program cochair for IACR CHES 2012, and has served as program chair for IEEE HOST 2011 and IEEE MEMOCODE 2009. He has served on the TPC of several conferences in this field including DAC, DATE, CHES and FPL, among others. He received a Best Paper Award at FPL 2011, and a 2007 CAREER Award from the US National Science Foundation. He is a senior member of the IEEE. His research interests include cryptographic engineering, embedded security, implementation attacks, side-channel analysis, fault analysis, hardware for cryptanalysis, hardware software codesign, custom-instruction set design, system-level design, design methodologies, reconfigurable computing.

Arun K. Somani is currently Anson Marston Distinguished Professor of Electrical and Computer Engineering at Iowa State University. Prior to that, he was a professor in the Department of Electrical Engineering and Department of Computer Science and Engineering at the University of Washington, Seattle, Washington and scientific officer for Govt. of India, New Delhi. He earned his MSEE and PhD degrees in electrical engineering from the McGill University, Montreal, Canada, in 1983 and 1985, respectively. Professor Somani’s research interests are in the area of computer system design and architecture, fault tolerant computing, computer interconnection networks, WDM-based optical networking, and reconfigurable and parallel computer systems. He has taught courses in these areas and published more than 250 technical papers, several book chapters, and has supervised more than 100 graduate students (35 PhD students). He is the chief architects of an anti-submarine warfare system for the Indian navy, Meshkin fault-tolerant computer system architecture, for the Boeing Company, Proteus multicomputer cluster-based system for the US Coastal Navy, and HIMAP design tool for the Boeing Commercial Company. He served as chair of the Department of Electrical and Computer Engineering at Iowa State University from 2003-2010. He also served on several program committees of various conferences, as IEEE distinguished visitor (2000-2002), IEEE distinguished tutorial speaker (2000-2002), IEEE Communication Society distinguished lecturer (2009-13). He has delivered several key note speeches, tutorials and distinguished and invited talks all over the world. He has served as editor of Electronic New Letter, IEEE Technical Committee on Fault Tolerant Computing (1992-97), associate editor of IEEE Transactions on Computers (1997-2002), associate editor of IEEE/ACM Transactions on Networking (2003-2009), associate editor of Elsevier Computer Networks Journal (2003-present), associate editor of Elsevier Optical Switching and Networking (OSN) (2004- present), and Member of Editorial Advisory Board, Journal of Pervasive Computing and Communications (2005-present). He received Canadian Commonwealth Fellowship for his graduate work during 1982-85. He was awarded Distinguished Engineer member grade of ACM, and elected fellow of IEEE for his contributions to “theory and applications of computer networks.”

Cheng-Zhong Xu received his BS and MS degrees from Nanjing University in 1986 and 1989, respectively, and PhD degree in computer science from the University of Hong Kong in 1993. He is a professor in the Department of Electrical and Computer Engineering of Wayne State University, the director of Laboratory for Cloud and Internet Computing. He is an adjunct professor of the Shenzhen Institute of Advanced Technology of Chinese Academy of Sciences, China and the founding director of the Center for Cloud Computing. His research interest is mainly in scalable distributed and parallel systems and wireless embedded computing devices, with an emphasis on resource and system management for performance, availability, reliability, energy efficiency, and security. He has published more than 180 articles in peer-reviewed journals and conferences in these areas, including over 20 papers in IEEE and ACM transactions. He is the author of book “Scalable and Secure Internet Services and Architecture” (Chapman & Hall/CRC Press, 2005) and a coauthor of book “Load Balancing in Parallel Computers: Theory and Practice” (Kluwer Academic/Verlag, 1997). He serves (served) on the editorial boards of a number of journals, including IEEE Transactions on Parallel and Distributed Systems, Journal of Parallel and Distributed Computing, and Journal of Parallel, Emergent, and Distributed Systems. He is an associate editor-in-chief of ZTE Communications. He served dozens of international conferences and workshops in capacity of program chair, general chair, and plenary speaker. He was a recipient of the Faculty Research Award, the President’s Award for Excellence in Teaching, and the Career Development Chair Award of Wayne State University, and the “Outstanding Oversea Scholar” award of National Science Foundation of China. He is a senior member of the IEEE.