Editor's Notice

IT IS A privilege to serve as Editor-in-Chief of one of the most prestigious journals in the computer field and to follow a distinguished and dedicated person such as Taylor Booth. With his able leadership over the past four years, he has made a remarkable contribution in maintaining and further improving the quality of the Transactions. We want to express our sincere appreciation for his four years of dedicated service to the Transactions.

As is the case of any high-quality journal, papers published in the Transactions undergo peer reviews prior to publication. To speed up the necessary review processes, the close cooperation of the authors, referees, and Editors is required. It is certainly helpful if an author supplies the required clean copies of a clearly written, well-organized manuscript, as well as all other necessary information. On the other hand, a referee must review the manuscript carefully and return it promptly. Even when he/she does not have time to review the manuscript, immediately returning it to the Editor will help to improve the time delay. On the part of Editors, we will try our best to shorten any unnecessary delay in processing manuscripts. One such possible delay occurs when our Editors and referees are overloaded. With this in mind, I am happy to announce the addition of seven new Editors to the Editorial Board. We are indeed fortunate to find such capable individuals to process and select papers for the Transactions. Brief biographies of these Editors follow.

On July 1, 1982, Prof. Wesley Chi will have served two consecutive two-year terms as Associate Editor (now Editor) for Distributed Processing and Computer Networks. During the past four years he has made a significant contribution to the content of the Transactions in these important disciplines. On behalf of the IEEE Computer Society I would like to express our sincere appreciation for his effort.

TSE-YUN FENG
Editor-in-Chief

Jean-Loup Baer (S'66-M'69) received the Diplome D'Ingenieur in electrical engineering and the Doctorat 3e Cycle in computer science from the University of Grenoble, Grenoble, France, in 1961 and 1963, respectively, and the Ph.D. degree from the University of California, Los Angeles, in 1968. Prior to joining the University of Washington, Seattle, in 1969, where he is currently a Professor of Computer Science, he was a Research Engineer with the Laboratoire de Calcul, University of Grenoble (1961-1963) and a member of the Digital Technology Group at U.C.L.A. (1966-1969). His present interests are in parallel and distributed processing, systems architecture, and data structures. He is the author of Computer Systems Architecture (Woodland Hills, CA: Computer Science Press).

Dr. Baer served as an IEEE Computer Society Distinguished Visitor during 1973-1975 and was an ACM National Lecturer from 1977 to 1978. He is a Guggenheim Fellow (1979-1980), an Editor of the Journal of Digital Systems, and an Associate Editor of the Journal of Computer Languages.

Jon T. Butler (S'67-M'67) was born in Baltimore, MD, on December 26, 1943. He received the B.E.E. and M.Eng. degrees from Rensselaer Polytechnic Institute, Troy, NY, in 1966 and 1967, respectively, and the Ph.D. degree from Ohio State University, Columbus, in 1973. Currently, he is an Associate Professor in the Department of Electrical Engineering and Computer Sciences at Northwestern University, Evanston, IL. His research interests include multiple-valued logic, fault diagnosis, and cellular array processing.

Dr. Butler has held two National Research Council Postdoctoral Associateships, one at the Air Force Avionics Laboratory at Wright-Patterson AFB, OH (1973-1974) and the other in the Department of Electrical Engineering, Naval Postgraduate School, Monterey, CA (1980-1981). He was Chairman of the 1980 International Symposium on Multiple-Valued Logic (ISMVL) and is Co-Chairman for North America of the 1983 ISMVL. He is Chairman of the IEEE Computer Society's Technical Committee on Multiple-Valued Logic.

Robert M. Keller (S'64-M'66) received the B.S. and M.S.E.E. degrees from Washington University, St. Louis, MO, and the Ph.D. degree from the University of California, Berkeley.

He is currently a Professor of Computer Science at the University of Utah, Salt Lake City. From 1970 to 1976 he was an Assistant Professor of Electrical Engineering at Princeton University, Princeton, NJ, and in 1974 he was a Visiting Assistant Professor at Stanford University, Stanford, CA. His primary interests lie in the area of asynchronous distributed systems, including their implementation, programming, verification, theory, and applications. Currently, these interests are manifest in the FGL/AMPS project, which entails research in construction of a usable general-purpose applicative language and its support on a distributed multiprocessor system.

Ming T. Liu (M'65-SM'82) received the M.S. degree in electrical engineering and the Ph.D. degree both from the University of Pennsylvania, Philadelphia, in 1961 and 1964, respectively.

From 1962 to 1969 he was an Instructor and an Assistant Professor of Electrical Engineering at the University of Pennsylvania. In 1969 he joined the Department of Computer and Information Science, Ohio State University, Columbus, where he is currently a Professor. Since 1973 he has been heavily involved in distributed processing and computer networking, publishing over 50 papers in this and related areas. He has designed and implemented two well-known local computer networks (DL.CN and DLILCN). Dr. Liu is a member of the Association for Computing Machinery (ACM). He was the Program Co-Chairman of the 1981 International Conference on Parallel Processing. He is a Guest Editor of a forthcoming special issue of the IEEE TRANSACTIONS ON COMPUTERS on parallel and distributed processing (December 1982). Currently, he is the Chairman of the IEEE Computer Society Technical Committee on Distributed Processing and an IEEE Computer Society Distinguished Visitor. He is also a member of the Society of Sigma Xi.
Amar Mukhopadhyay received the D.Phil.(Sc.) degree from the Institute of Radiophysics and Electronics, University of Calcutta, Calcutta, India, in 1963. He held faculty positions at Princeton University, Princeton, NJ; in computer science, University of Iowa, Iowa City, and is now with the University of Central Florida, Orlando, where he has been a Professor of Computer Science since 1979. He held research positions with Tata Institute of Fundamental Research, Bombay, India, English Electric Research Laboratory, London, England, and Stanford Research Institute, Palo Alto, CA, and consulting positions with Harris Corporation, Melbourne, FL. His earlier research interests were in switching theory, logic design, and cellular logic. He is the Editor and coauthor of Recent Development in Switching Theory (New York: Academic, 1971). His current interests include VLSI hardware algorithms, architecture, and design methodologies.

Dr. Mukhopadhyay was also an Associate Editor of the IEEE Transactions on Computers during 1971 through 1974.

Earl E. Swartzlander, Jr. (S’64-M’72-SM’79) received the B.S.E.E. degree from Purdue University, Lafayette, IN, in 1967, the M.S.E.E. degree from the University of Colorado, Boulder, in 1969, and the Ph.D. degree in computer design from the University of Southern California, Los Angeles, in 1972.

He is the Manager for Technology and Advanced Development in the Systems Engineering Operations of the TRW Defense Systems Group. This involves the conceptual definition and development of advanced signal and data processing systems. His current activity focuses on distributed processing systems, including system architecture, hardware, and software development. He has directed the development of a variety of advanced signal processing systems, and has developed the architectural and functional design of VLSI components which are currently in development. He is a Consultant to the Biodynamics Research Unit of the Mayo Clinic, and was a member of the Navy PME-124 panel on Programmable Signal Processors in 1976. He has taught short courses on VLSI architecture and microprocessors for American University, UCLA, University of Maryland, and University of Michigan. He has directed the development of several projects including the Digital Beam Former Emulation Model for BMD, the AFAL Micro-Signal Processor, and the SOSUS Sonar Adaptive Equalizer Breadboard for the Navy. In addition, he has developed a special-purpose computer for avionics human factors research and performed the detailed design and checkout of the electronic system of a solar spectro-heliometer used on Skylab. He also supervised the prototype construction, checkout, and initial production of a multiplexed polling system for commercial security monitors, and was the Chief Engineer for a geophysical exploration firm with responsibility for systems design and field support of an advanced seismic exploration system. He has published over 40 papers in the fields of computer architecture, VLSI implementation, and computer arithmetic, and is the Editor of the books Computer Design Development (New York: Hayden, 1976) and Computer Arithmetic (Stroudsburg, PA: Dowden, Hutchinson & Ross, 1980). He is a Registered Professional Engineer in Alabama, California, and Colorado.

Dr. Swartzlander is quite active in the IEEE Computer Society as Chairman of the Technical Committee on Data Acquisition, General Chairman of the Real-Time Systems Symposia, and Chairman of Distributed Systems Sessions at a variety of conferences. He is a member of ACM, Eta Kappa Nu, Sigma Tau, and Omicron Delta Kappa.

Chuan-lin Wu (S’74-M’78) received the B.S. and M.S. degrees in electrical engineering from Chiao Tung University, Taiwan, China, and the Ph.D. degree in electrical and computer engineering from Wayne State University, Detroit, MI.

He was an Assistant Professor at Wayne State University, Wright State University, and Ohio State University and is now on the faculty of the Department of Electrical Engineering, University of Texas, Austin. His current research interests include interconnection networks, computer architecture, and parallel/distributed processing.

Dr. Wu has served as a Guest Editor for the Computer magazine, a member of the IEEE Computer Society Publications Board, a Program Committee member or Session Chairman for various technical conferences, and as a reviewer for a number of journals. Currently, he serves as a member of the Computer Society Press Advisory Committee and as a Vice Chairman of the Technical Committee on Distributed Processing. He is a member of Phi Tau Phi and ACM.