Acknowledgements

VLSI, like any complex organization, is the sum of the efforts of a large number of volunteers, who selflessly have volunteered their time with their only reward essentially being the satisfaction of seeing a job well done, basking in the realization that they have contributed to the dissemination of scientific knowledge through the continued success of a forum dedicated to the exchange of advances in both research and practice in VLSI Test. Insufficient though of course it is, compared to the magnitude of the efforts displayed by the volunteers, we would nonetheless like to register herein a small note of thanks to the whole body of volunteers on whose efforts we have come to deeply rely.

Among those volunteers, we would like to especially note a subset who have stood out by helping us with a body of important organizational tasks. Namely,

Anand Raghunathan, Christian Landrault and Subhasish Mitra for their role as site coordinators for the VTS Program Committee paper selection meeting.

Ramamurthi Chandramouli, Mick Tegethoff and Burnie West for their continued contributions to the efforts for the publicity of VTS.

Yervant Zorian, Juan Figueras, Andre Ivanov, Michalis Nicolaidis, Rob Roy, Adit Singh, and Prab Varma, the VTS 2007 Steering Committee, for their continued advice and insight on all aspects of the VTS organization.

In addition to these volunteers, VTS would not have been possible without the efforts of a number of business organizations on whose first rate professional efforts we have come to rely. Namely,

Maddie Harwood, Dana Hutchinson, Meredith Griffith, and Carol Payne of CEM Inc. for administrative help with various aspects of the VTS organization.

Stefano di Carlo and Giorgio di Natale as web masters of the paper submission, review, selection and publicity efforts.

We also acknowledge the generosity of NEC USA, LIRMM, Virage Logic and Stanford University for hosting the VTS Program Committee paper selection meeting.

But most importantly, we would like to thank all of you, the VTS 2007 participants, the VTS 2007 paper submitters, authors and speakers, session chairs, and panel organizers for making the VLSI Test Symposium a continued success and establishing it as the preeminent forum for the exchange of innovative ideas in all aspects of VLSI Test.