Session 12C — IP Session: Making the (Yield) Difference: DFY/DFM

Organizer: R.Chandramouli, Virage Logic

Presentations:

Design Planning and Techniques for Yield Improvement
Jun Qian, Cisco, San Jose

Yes, Virginia, there is EDA in DFM/DFY
Marc Levitt, Cadence Design, San Jose

Enabling Design-Stage Yield Improvement using Unified Yield Modeling Platform
Ara Markosian, CTO, Ponte Solutions, Inc.