Invited Presentation

Ronnie Vasishta
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Mr. Vasishta is the Vice President of technology marketing for LSI Logic Corporation. He is responsible for company-wide strategic technology direction and direct marketing support for customers, sales force, vertical market groups and design centers. He is responsible for the definition and development of the LSI logic’s technology products including: silicon technologies, advanced packaging, mixed signal, embedded memory, I/Os and methodologies.

Mr. Vasishta joined LSI Logic in 1987 as a process development and engineering manager in the European wafer fabrication facility. He advanced to LSI Logic R&D division as the European technology transfer manager, responsible for the transfer of silicon technologies, packaging technologies and design methodologies to LSI Logic customers. Mr. Vasishta served as LSI Logic’s marketing manager in Europe, positioning the company’s silicon, packaging and design methodology products to its European customer base.

Before joining LSI Logic, Mr. Vasishta held management positions at Motorola Incorporated and STC Semiconductors in UK. Mr. Vasishta graduated in 1985 from Trent University in Nottingham, United Kingdom with a Bachelor of Science degree in electrical engineering.

Abstract
The increasing complexity and functionality trends of SOC are placing increasing demands on test coverage, complexity, and reliability. With low voltages, high densities, and multi-level metals, traditional stuck-at testing will need to be improved. New fault models will be needed to address crosstalk, delay and skew variations, and undetermined signal voltage. In addition, rising tester equipment costs are significantly raising the ATE cost per signal-pin on high-performance machines. Low pin-count tester strategies are needed to address this.