Welcome to the 28th International Conference on VLSI Design!

This year, the conference is being held in Bangalore, the city with the largest presence of the semiconductor industry in India. In this connection, we are particularly fortunate to have received a very enthusiastic response from the industry, which is quite clear from the participation of various companies in the organization of the conference, and from their contributions to the final technical program.

We received a substantially large number of submissions to the conference this year. A total of 313 papers were submitted, which were reviewed by a pool of 300 reviewers and track chairs. Each paper received a minimum of three independent reviews. In order to provide a fair review to all the submissions, papers that received mixed reviews from individual tracks were discussed again in the program committee meetings, before a final decision was taken. As in the previous years, the paper selection meetings were concurrently held in India and the United States, in the cities of Bangalore, and Princeton, New Jersey. After much deliberation and discussion, the committees selected 72 papers for presentation at the conference, with an overall acceptance rate of 23%. In addition, another 18 papers were selected for poster presentation and are also included in the final program.

This year, the technical program of the conference will be spread over three days and will have twenty-four sessions. The technical program will be preceded by two days of tutorials, and, concurrent with the final two days of the conference, RASDAT-2015 will be organized.

In keeping with the general theme of the conference on the internet of things (IoT), we are happy to present a substantial number of papers in all areas of the conference that adhere to this theme. The Analog, Devices/Circuits and Digital/FPGA sessions will report several new advancements in the areas of signal generation/conditioning, power supply management and design with advanced process technologies. In the system design and the high performance computing areas, advances in hardware and software technologies are reported. Papers in the areas of EDA, test and reliability, address traditional research issues and also some of the hot topics of fault diagnosis, soft errors, and yield optimization. There will be special sessions on IoT and sensors, through which, we hope to highlight the opportunities that exist in research and product design, in this fast evolving field. We also have a special panel that will discuss technological advances in some of the fast developing countries like BRICS.

Once again, we extend a very warm welcome to you. We hope that your stay in Bangalore is pleasant, and your attendance of the conference, truly worthwhile.

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