Industry Standards from Accellera

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Abstract:

Accellera's [www.accellera.org](http://www.accellera.org) mission is to drive worldwide development and use of standards required by systems, semiconductors and design tools companies, which enhance a language based design automation process.

Overview of different standards from Accellera and how they fit into the design flow process will be presented. Status of various technical sub-committees like Open Compression Interface (OCI), Unified Power Format (UPF), Unified Coverage Interoperability (UCIS) and Open Verification Library(OVL) will also be covered.

Speaker Bio:

Shrenik Mehta is Senior Director of Frontend Technologies and OpenSPARC program at Sun Microsystems. Shrenik has been driving the details of the OpenSPARC project from its infancy to the public release. Shrenik is also currently the Chair of Accellera, an industry standards organization and was the Vice Chair of the IEEE 1800 SystemVerilog Working Group. He holds eight US patents and one patent in Taiwan.

He has a Master's degree in Computer Engineering from University of Michigan, Ann Arbor and a Bachelor's degree from Institute of Technology - BHU, Varansasi (India).

[Editorial note: The Verilog-AMS standard from Accellera will be covered by Sri Chandra of Freescale and fellow speaker]