

VLSI Design 2001 Conference Awards

Arun Kumar Choudhury Best Paper Award

Estimating Crosstalk from VLSI Layouts
C.P. Ravikumar and V. Shankar Subramanian

Best Student Paper Award

Combinational Test Generation for Acyclic Sequential
Circuits Using a Balanced ATPG Model
Yong Chang Kim, Vishwani D. Agrawal, and Kewal K. Saluja

Honorary Mention Award

Observability Register Architecture for Efficient
Production Test and Debug of VLSI Circuits
Dilip Bhavsar and Rishan Tan