Message from the WAMCA 2016 Workshop Chairs

It is with great pleasure that we meet again for the seventh edition of the Workshop on Applications for Multi-Core Architectures (WAMCA 2016) held in conjunction with the International Symposium on Computer Architecture and High Performance Computing in Los Angeles, USA. Motivated by the emergence and pervasiveness of the multi- and many-core architectures, our yearly workshop focuses on both hardware and software techniques to effectively exploit the increasing number of cores made available in accelerators and mainstream processors.

Our main mission is to provide a forum to present and discuss the state-of-the-art results in multi- and many-core architectures, tools, and applications. Hence, we selected 9 papers from a total of 11 high-quality submissions. We are very enthusiastic about the quality of the submitted papers. Their subjects included network-on-chip proposals, parallelization techniques, heterogeneous systems, applications, distributed many-core systems, and GPU-based systems.

We would like to thank the members of the Program Committee and the ad hoc reviewers for the great work in providing insights and suggestions for the authors. We really feel like the quality of reviews was first rate and, from their content, we can only look forward to the presentations and discussions during the upcoming event.

See you in L.A.!

Claude Tadonki, MINES ParisTech
Cristiana Bentes, State University of Rio de Janeiro
Guido Araujo, State University of Campinas
Lucia Drummond, Fluminense Federal University
Mauricio Pilla, Federal University of Pelotas
Phillipe Navaux, Federal University of Rio Grande do Sul
Ricardo Farias, Federal University of Rio de Janeiro