Message from the MPP 2016 Workshop Chairs

The increase in the number of processor cores has opened up new opportunities to take advantage of the parallelism available in modern data-centric applications and workloads. On the other hand, writing parallel programs is still an error-prone and challenging task for the typical programmer. Novel programming/execution models, which ease the job of the programmer while exposing the hardware resources to the compiler/runtime/OS stack, are the key to unleashing the parallelism potential of future applications. In recent years task-based programming models (e.g., OpenMP and OpenSs) have been shown to be scalable and flexible approaches for extracting task parallelism from applications.

MPP 2016 brings together researchers and practitioners interested in developing novel computational models for parallel programming and architectures. Given the growing interest in task parallelism programming and execution models, MPP 2016 dedicated this edition to such a programming model.

Cheers!

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