Message from the MPP Chairs

Writing parallel applications is not a trivial task, requiring a deeper knowledge about algorithms, operating and runtime systems, data placement, and efficient use of hardware. A bottleneck at any level can easily nullify the benefits of parallelism – thus posing a challenge to the scientific and industrial communities. The creation of models and alternatives to ease parallelism exploitation still requires a tremendous effort from programmers. Those models should exploit concurrency in a natural way, while hiding memory and synchronization latencies, leading to simpler and more power efficient systems. Moreover, they should provide more powerful, flexible and understandable parallel programming constructs. MPP 2015 - 4th Workshop on Parallel Programming Models - aims at bringing together researchers and practitioners interested in novel computational models to extract parallelism from applications.

Cheers!

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