Program Chairs’ Introduction

This volume contains the papers presented at the Third Euromicro Workshop on Parallel and Distributed Processing, held in San Remo, Italy, January 25-27, 1995.

Parallel and distributed processing is crucial for high performance computing. This area has experienced many developments in the last decade. Nowadays a variety of different architectures, programming languages, operating systems, tools and applications are commercially available. However, this is not a consolidated area. Parallel and distributed processing is a very active research area with many new ideas continuously emerging. This third Euromicro workshop aims at providing a forum to exchange ideas, discuss key issues and present the latest research results in parallel and distributed processing.

The workshop includes 2 keynote sessions with invited eminent speakers; 3 sessions for open forum presentations; group and plenary discussion sessions; and 7 full paper presentation sessions covering the most important topics on parallel and distributed processing: parallel algorithms, load balancing, software environments, modeling, operating systems, and parallel architectures. In addition, there are 3 special sessions on massively parallel computer systems, dependable parallel systems and linear algebra.

There were 136 papers submitted to the workshop. This number reflects a significant increase in relation to previous editions of the workshop, which confirms the increasing interest of the scientific community for this subject. Each paper was forwarded to three members of the program committee and one of the authors of another paper in order to be reviewed.

Despite the time of year in which the reviews were requested (August), the collaboration of the reviewers was extremely efficient. In average, we received about 3.3 review reports per submitted paper. The program committee met in Liverpool on September 8th, 1994, and based on those reviews, 38 papers were selected for full paper presentation, and a further 33 were selected for presentation in open forum sessions.

We would like to thank many people who made a contribution to this workshop. We are indebted to our colleagues Anna Antola and Nello Scarabottolo at the Universita degli Studi di Pavia and the Politecnico di Milano respectively, for their excellent organization of the local event. We are also grateful to the members of the program committee who did a very difficult job reviewing many papers and setting up the program.

We would like to thank all the additional reviewers and session chairpersons for their work. Special thanks to our support team in the Computer Architecture Department and the European Center for Parallelism in Barcelona, in particular to Jordi Tubella and Cristina Coll for their constant help. We would also like to think Chiuita Snipe-Marlisha, the administrative manager of Euromicro, for her support, as well as the whole Euromicro organization. Finally, thanks to Bob Werner at the IEEE Computer Society Press for his effort in elaborating this volume.

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