Abstract
The increasing demands on embedded non-volatile memories in logic circuits spurred many research activities in the development of low-cost, highly scalable and compatible solutions. Besides full compatibility to standard CMOS logic process, low voltage and power operations, small cell size, and fast programming and accessing speed are all desirable features in logic NVMs. As technology scales, conventional solutions suffer from high power, performance degradation and poor scalability. In this talk, new logic NVM cells developed for advance logic circuit are introduced and compared to provide new design directions for the future.

Biography
Ya-Chin King was born in Taiwan, Republic of China. She received the B.S. degree in electrical engineering from National Taiwan University in 1992, and the M.S. degree in electrical engineering from University of California, Berkeley, in 1994. She received her PhD degree in May of 1999, at University of California, Berkeley, on thin oxide technology and novel quasi-nonvolatile memory. She joined the faculty of National Tsing-Hua University at Hsinchu, Taiwan in August 1999. She is currently a professor of the electrical engineering department at NTHU. Her research topics include: advance gate dielectric, CMOS image sensor and non-volatile memory design.