Message from the IEEE MCSoc-15
Program Co-Chairs

Welcome to the 2014 IEEE 8th International Symposium on Embedded Multicore/Many-core System-on-Chip (MCSoC 2015), held in Turin, Italy, 23-25 September 2015. This symposium aims at providing the world’s premier forum of leading researchers not only in embedded Multicore/Many-core SoCs, but also in their software, tools and application design areas for academia and industries. Embedded system designs have evolved over time from fairly simple single-core single memory based designs to multi-core processing units connected by an on-chip network on the same silicon including heterogeneous computational units. The number of cores to be integrated in a single chip is expected to rapidly increase in the coming years, moving to multicore/many-core heterogeneous architectures. This requires a global rethinking of software and hardware design approaches. From on the application side, multicore/manycore embedded systems are pushing developments in various domains such as biomedical, health care, internet of things, smart mobility, and aviation.

The symposium received large numbers of submissions. All submissions were carefully reviewed by program committee members. To maintain the high quality of the symposium, we finally decided to accept only forty one papers for oral technical presentations. We also planned four keynotes and a couple of invited talks for special sessions. We believe all of these papers and topics will not only provide novel ideas and new results but also stimulate future research activities in the area of multicore design in particular and embedded systems.

The program for this symposium is the result of hard and excellent work of many people including authors, organizing committee members, and program committee members. We would like to express our appreciation to all of them for their great help and cooperation.

IEEE MCSoc-15 Program Co-Chairs:

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