
Systems on chips designs have evolved from fairly simple single core, single memory designs to complex Multi-core systems on chip consisting of a large number of IP blocks on the same silicon. As more and more cores are integrated into these designs to share the ever increasing processing load, the main challenge lies in efficiently and quickly integrating them into a single system capable of leveraging their individual flexibility.

The IEEE MCSoC-12 Symposium aims at providing the world’s premier forum of leading researchers in the Multi-core System-on-Chip design areas for Academia and industries.

The Symposium received large numbers of submissions representing many different countries. All submissions were carefully reviewed by program committee members. To maintain the high quality of the Symposium, we finally decided to accept only thirty four papers for oral technical presentations, including papers from five special sessions. We believe all of these papers and topics will not only provide novel ideas and new results but also stimulate future research activities in the area of Multi-core design in particular and embedded world in general. The program for this Symposium is the result of the hard and excellent work of many people including organizing committee members, program committee members, and authors. We would like to express our appreciation to all of them for their great help and cooperation.

Toshiyaki Miyazaki, IEEE MCSoC-12 Program Chair
Ben Abdallah Abderazek, IEEE MCSoC-12 Steering Chair