Message from the IEEE MCSοC-12 Honorary Chair

It is my pleasure to welcome you to the IEEE 6th International Symposium on Embedded Multicore Systems-on-Chip (MCSοC-12). This event started in 2006 as a small workshop (SoC-06) about Systems on Chip in conjunction with the International Conference on Advances in Mobile Computing and Multimedia (MoMM 2006), and then held in conjunction with the International Conference on Parallel Processing (ICPP) until the 2010 Symposium, which was organized in San Diego, California, September 13-16, 2010. The 2011 Symposium was postponed due to the Great East Japan Earthquake.

The main goal of the IEEE MCSοC-12 symposium is to gather a large number of world-renowned experts and young researchers in the embedded multicore research areas, for the dissemination of novel results, exchange of scientific ideas among the participants from Japan and abroad.

The University of Aizu established a cooperative exchange by forming partnerships with universities and institutions overseas and by encouraging academic and research exchanges. The Symposium creates the necessary condition to maintain international scientific contacts at the student level by offering a marvelous opportunity for the best university students to make their first scientific presentations, thus serving as a training ground for both Japanese and foreign students on their way to becoming scientists. Common discussions between world leading researchers and students allow our students to learn how to conduct research, how to prepare results, how to present them to the public as well as how to proof the results of their achievement. We do hope that some students who have participated in this Symposium will maintain their cooperative relationship in the future.

Let me sincerely thank all of the participants for their contributions and efforts to maintain the high scientific level of the joint International Symposium on Embedded Multicore SoCs (IEEE MCSοC-12).

Shigeaki Tsunoyama, IEEE MCSοC-12 Honorary Chair