The Challenges of Design and Test
For the World Wide Web

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As the world leader in microprocessors, Intel has long been at the forefront of development, validation, and testing of microprocessor products in various platforms. The complexity of microprocessor designs is burgeoning: new uses for computers, less technically sophisticated users who are, therefore, more demanding, new power dissipation constraints, ever higher performance goals, much larger volumes shipping in less time, and the addition of the internet and associated security enhancements. All of these further complicate the way validation and testing are undertaken. At the same time, the prices of PCs are dropping dramatically. With this in mind, Intel is investigating new test methodologies and we challenge the testing industry to keep pace with the new products and test environments through improvements in test equipment capability and cost effectiveness as well as test methodologies. Pat’s discussion will take the audience through component, system, and internet environments, and the challenges they present for validation, test, and the associated success of a product in the marketplace.

About the Speaker

Mr. Pat Gelsinger is Vice President and General Manager of Intel Corporation’s Desktop Products Group. Mr. Gelsinger, who joined Intel in 1979, has over 19 years of experience in general management and product development positions. From 1992 to 1996, Pat was instrumental in defining and delivering the Intel ProShare® video conferencing product line. Prior to 1992, Pat was General Manager of the division responsible for the Pentium® Pro, IntelDX2™, and Intel486™ microprocessor families. Other positions held during Pat’s Intel career include: Director of the Platform Architecture Group, design manager and chief architect of the original i486™ microprocessor development, manager of CAD methodologies, and key contributor on the original i386™ and i286 chip design teams.

Mr. Gelsinger holds six patents and more than a dozen applications in the area of VLSI design, computer architecture and communications. He has more than 20 publications in this field including “Programming the 80386,” published in 1987 by Sybex, Inc. He has received numerous Intel and industry recognition awards. His promotion to Group Vice President at age 32 made him the youngest vice president in the history of the company.

Pat holds an AA degree from Lincoln Technical Institute (1979), a B.S. Magna Cum Laude from Santa Clara University (1983), and an M.S. from Stanford University (1985). All degrees are in Electrical Engineering. Pat is married and the father of four children.