Welcome to the 5th ICPP Workshop on Compile/Run Time Techniques for Parallel Computing (CRTPC-2006) held in conjunction with “The 2006 International Conference on Parallel Processing” (ICPP-06) in Columbus, Ohio, USA, on August 14-18, 2006.

It is widely recognized that although the peak performance of parallel systems continues to advance steadily, the sustained realization of high efficiency on parallel systems continues to be a difficult challenge. Achieving high efficiency generally requires explicit attention to considerable detail and the use of message passing, which is very tedious for the development of complex applications. Hence, it is extremely important to develop compile-time and run-time techniques that can ease the burden of the programmers of parallel systems, and help achieve high efficiency.

The workshop features eight papers, from researchers in Asia, Europe, and North America in broad areas of compile and run-time techniques. We thank the authors for their contributions to the workshop, and the referees for their constructive comments. We also thank Dr. Jarek Nieplocha from Pacific Northwest National Laboratory on agreeing to deliver the keynote talk at the workshop.

Vipin Chaudhary and P. Sadayappan
Workshop Co-Chairs