Welcome to the ICPP Workshop on Compile/Runtime Techniques for Parallel Computing.

It is widely recognized that although the peak performance of parallel systems continues to advance steadily, the sustained realization of high efficiency on parallel systems continues to be a difficult challenge. Achieving high efficiency generally requires explicit attention to considerable detail and the use of message passing, which is very tedious for the development of complex applications. Hence it is extremely important to develop compile-time and run-time techniques, that can ease the burden of the programmers of parallel systems, and help achieve high efficiency.

The workshop features nine papers, from researchers in Asia, Europe and North America. The topics cover both compile-time techniques such as loop tiling, data distribution and use of high level programming methodology, and runtime techniques for work distribution and thread migration. We thank the authors for their contributions to the workshop, and the referees for their constructive comments.

Vipin Chaudhary and P. Sadayappan
Workshop Co-Chairs