Keynote 1: On Fault Resilience of Cloud Computing

Kang G. Shin
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Biography

Kang G. Shin is the Kevin & Nancy O’Connor Professor of Computer Science in the Department of Electrical Engineering and Computer Science, The University of Michigan, Ann Arbor. His current research focuses on QoS-sensitive computing and networking as well as on embedded real-time and cyber-physical systems.

He has supervised the completion of 74 PhDs, and authored/coauthored more than 800 technical articles (more than 300 of these are in archival journals), a textbook and more than 20 patents or invention disclosures, and received numerous best paper awards, including the Best Paper Awards from the 2011 ACM International Conference on Mobile Computing and Networking (MobiCom’11), the 2011 IEEE International Conference on Autonomic Computing, the 2010 and 2000 USENIX Annual Technical Conferences, as well as the 2003 IEEE Communications Society William R. Bennett Prize Paper Award and the 1987 Outstanding IEEE Transactions of Automatic Control Paper Award. He has also received several institutional awards, including the Research Excellence Award in 1989, Outstanding Achievement Award in 1999, Distinguished Faculty Achievement Award in 2001, and Stephen Attwood Award in 2004 from The University of Michigan (the highest honor bestowed to Michigan Engineering faculty); a Distinguished Alumni Award of the College of Engineering, Seoul National University in 2002; 2003 IEEE RTC Technical Achievement Award; and 2006 Ho-Am Prize in Engineering (the highest honor bestowed to Korean-origin engineers).

He was a co-founder of a couple of startups and also licensed some of his technologies to industry.
Keynote 2: The Architecture for Discovery in a Parallel Universe

Joseph Curley
Director, Technical Computing Group Marketing (TCGM)
Datacenter and Connected Systems Group
Intel Corporation
Hillsboro, Oregon, U.S.A.

Biography

Joseph (Joe) Curley serves Intel® Corporation as director of marketing for Technical Computing Group. The technical computing group marketing team manages marketing for high-performance computing (HPC), and workstation segments and product lines, as well as future Intel® Many Integrated Core (Intel® MIC) products.

Joe joined Intel in 2007 to manage planning activities that lead up to the announcement of the Intel® MIC Architecture in May of 2010. Prior to joining Intel, Joe worked at Dell, Inc. leading the Dell Precision Workstation and consumer / small business desktop products, as well as a series of engineering roles. He began his career at graphics pioneer Tseng Labs.