Welcome to ICCD '95

Welcome to ICCD '95 in its new venue of Austin, Texas. ICCD is a multi-disciplinary conference emphasizing the interactions among architecture, networking, computer-aided design, design and test, technology and embedded computing. This year, we received 206 submissions from around the world: 153 from North America, 33 from Europe, 19 from Asia, and one from South America. Of those, the technical program committee accepted 101 papers for presentation at the conference. This broad participation demonstrates ICCD's continuing concern with supporting the EE/CS professional with a truly international event serving an interdisciplinary audience from both industry and academia.

This year's conference introduces several new program features. The new track on Data Communications and Networks covers topics in networking ranging from multiprocessor networks to global networking. The conference also features half-day tutorials on topics critical to the computer system designer: “Superscalar Processor Design,” by Prof. John Shen of Carnegie Mellon University; “Communication Paradigms in Parallel Systems,” by Prof. Joergen Staustrup of the Technical University of Denmark, Lyngby; “Advanced 3D Multichip Module Technologies for Integrated Computing Systems” by Dr. Leon Alkalai of NASA/JPL California Institute of Technology; “High-Level Synthesis: Methodology and Algorithms – From Research to Production Use,” by Dr. Reinaldo Bergamaschi of IBM T.J. Watson Research Center; and “IDDQ Testing,” by Prof. Chuck Hawkins of University of New Mexico. The conference keynote speaker will be Dr. Larry Loucks, who is an IBM Fellow and Vice President of Software Architecture, Personal Software Products at IBM Austin. He will speak on “Hardware Support for Emerging Software Technologies,” a topic at the heart of ICCD’s cross-disciplinary focus.

Plenary sessions provide reviews of the latest developments and visions of future progress by leaders in the field. This year’s plenary speakers include: Nick Naclerio of ARPA on “Advances In Semiconductor Packaging and their Impact on System Design”; Benjamin Wah of the University of Illinois on “Statistical Generalization: Theory and Applications”; Bill Mangione-Smith of UCLA on “Technical Challenges of PDA Design”; Sudhakar Reddy of the University of Iowa on “Testing – What’s Missing? An Incomplete List of Challenges;” and Dr. Bedrich Hosticka of FhG IMS Duisburg on “Towards Integrated System Design: A Global Perspective.” The conference will also feature a panel on “Frontiers of Multimedia Computing” organized by Dr. P.A. Subrahmanyan of AT&T Bell Laboratories. The conference banquet speaker will be Prof Steve Szygenda of University of Texas at Austin who will talk about “The Risk of Being and Engineer – Ever Wondered about How Product Litigation Could Affect Your Work?”

The technical program features a panel session titled “High Performance Microprocessors: Comparative Design Trends” moderated by Prof. Edward S. Davidson, Univ. of Michigan, and with the following panelists: Bob Colwell, Intel Corp.; Joel Emer, DEC; Robert Garner, SUN Microsystems, Inc.; Greg Grohoski, Cyrix; Pete Hsu, SGI; and Chuck Moore, IBM. This panel will discuss alternate design strategies and achieved cost/performance metrics, based on experience gathered from past and current high-end microprocessors. It brings together lead designers and architects from six major vendors in the processor arena. It will be moderated by the eminent computer architect, Prof. Edward S. Davidson, whose pioneering work in the area of pipelined machines forms the core of many high-performance CPU organizations in use today.

Technical sessions will include technical papers on topics of critical interest to computer system designers. ICCD '95 will include sessions on: both the PowerPC™ and UltraSPARC™ architectures; low-power design; signal processing architectures; massively parallel processing interconnects; ATM and high-speed networks; high-level synthesis; placement and routing; memory system performance; emerging technologies for processor verification; fault simulation; performance-driven memory; asynchronous systems; embedded computing system analysis; case studies in embedded computing; formal verification techniques in the real world; superscalar processor architectures; simulation techniques; combinational and sequential logic optimization;

1995 marks the first year in which ICCD will be held in Austin, Texas. We hope that the move will introduce the conference to new audiences while we maintain contact with our existing audiences. Austin and the rest of Texas have vital computer and semiconductor industries which provide a lively forum for ICCD. Austin’s rank as a center for live music provides entertaining options for extracurricular activities.

We would like to thank the Technical Program Committee, the Executive Committee, the IEEE Circuits and Systems Society, and the IEEE Computer Society for their enthusiastic support and contributions. Special thanks to all the authors, speakers, session chairs, and the audio visual team, who make the conference what it is. Of course, we would like to thank all the attendees for participating in ICCD and wish them a productive and enjoyable conference. We would appreciate any feedback you have on the conference – we hope to make ICCD '96 even better.

Tony Ambler, General Chair
Wayne Wolf, Technical Program Chair