Message from the Organizing Committee

We welcome you to the Fourth IEEE International Symposium on High Performance Distributed Computing (HPDC). This HPDC symposium, initially established in 1992, has flourished to become the leading international forum for presenting the latest research findings that focus on the synergy between parallel and distributed computing and high speed computer communications.

We are excited about the growing research and application opportunities offered by the increasing number of very high speed networks (gigabit testbeds) being built by industry, academia, and/or government. The HPDC symposium continues to provide a unique opportunity for researchers, application developers, and users from the parallel processing, distributed computing, and computer communications disciplines to exchange ideas and learn from each other.

In this year’s symposium, we have set up a HPDC environment built around an ATM local area network and a high speed link to the NYNET testbed, an ATM High-Speed Wide Area Network in New York State. This HPDC environment will be used to demonstrate interesting HPDC research results and applications during a single track session, HPDC Demonstrations.

This year, we continue our tradition of featuring distinguished keynote and invited speeches. Our first keynote address will be given by Lionel Skipwith Johns, Associate Director for Technology in the Office of Science and Technology Policy within the Executive Office of the U.S. President. A second keynote address will be presented by Walter Johnston, Vice President, NYNEX Science and Technology. The invited presentations will be given by Jamshed Mirza, IBM; Paul Woodward, University of Minnesota; and Ira Richer, MITRE Corp.

The success of the conference depends on the contributions of many individuals who put together an outstanding program. We would like to acknowledge the excellent job done by Andrew Grimshaw, the Program Chair; C.S. Raghavendra, Exhibits Chair; Vaidy Sunderam, Tutorials Chair; T.V. Lakshman, Walid Dabbous, and Makoto Takizawa, the Publicity Chairs; and Peggy Van Arnam and Chris Adamas, Registration and Local Arrangements Chairs. We would also like to acknowledge the enormous support of Maggie Johnson and Mary E. Kavanaugh at the IEEE Computer Society.

Finally, we would like to thank our sponsors, the IEEE Computer Society and Syracuse University. Many thanks are due to ACM SIGCOMM and Rome Laboratory for their support and to our industrial contributors IBM, AT&T, nCUBE, and Cray Research Corp.

On behalf of the organizing committees, we extend our invitation to our colleagues pursuing research in HPDC fields to join us as we develop HPDC technologies for the 1990s and beyond!

Salim Hariri
Steering Committee Chair
Electrical and Computing Engineering
Syracuse University
2-125 Center of Science and Technology
Syracuse, NY 13244-4100
E-mail: hariri@npac.syr.edu

Geoffrey C. Fox
General Chair
Northeast Parallel Architectures Center
Center of Science and Technology
Syracuse, NY 13244-4100
E-mail: gcf@npac.syr.edu