

Message from the General Chair

On behalf of the Organizing Committee, we are very pleased to welcome you to HPC Asia 2004, The 7th International Conference on High Performance Computing and Grid in Asia Pacific Region, being held in Omiya Sonic City, Tokyo-area, Japan, during July 20-22, 2004. It is sponsored by SIGHPC/Information Processing Society of Japan and IEEE CS Japan Chapter. It is also supported by several academic organizations.

High performance computing in liaison with the grid technology continues to demonstrate dramatic growth and change worldwide, as more users harness its power and capability in more applications. In view of this situation, users are required to utilize systems with higher performance, while vendors are required to provide systems with better performance and robustness in heterogeneous and often fragile environments. As the technology develops, wide variety is added to architecture, network, system software, middleware and application software. Now is a most appropriate time to have a chance to discuss about these topics.

HPC Asia is an international conference series held every 18 months in an Asia-Pacific regional site: Taipei (1995/9), Seoul (1997/4), Singapore (1998/9), Beijing (2000/5), Gold Coast (2001/9) and Bangalore (2002/12). It provides a forum for HPC researchers, developers, and users throughout the world to exchange ideas, case studies, and research results related to all issues of high performance computing. The current conference in Japan is the seventh in this series and we decided to add the word “Grid” to the title of this conference in view of remarkable development in this field. Several workshops are planned as mini-symposiums of the conference.

The conference is your unequalled opportunity to gain a deep insight into the high performance computing and grid. The comprehensive program brings you timely intelligence on the most recent advances in this field. The speakers will share their expertise, insight and experience with you and will offer you stimulating visions of the future of high performance computing and grid.

Thank you for your participation in HPC Asia 2004.

Yoshio Oyanagi
Professor, The University of Tokyo
HPC Asia 2004 General Chair