Program Chair’s Message

Welcome to Bangalore and the Fourth International Conference on High Performance computing. This year’s conference features 6 keynote addresses, 12 contributed paper sessions, a special session on instruction-level parallelism, a panel on network computing, a mini panel on instruction-level parallelism, 2 industrial-track sessions, a poster session, and 7 tutorials.

The 76 contributed papers to be presented at the conference were selected from the 132 submissions received from over 20 countries. The contributed papers were assigned to program committee members who in turn sent the papers to over 250 experts for evaluation. These expert evaluations together with the recommendations of the program committee members were used in arriving at the final program.

This year’s keynote speakers are: Dr. Dileep Bhandarkar, Director, System Architecture, Intel Corporation; Dr. Mick Dungworth, Vice President, SGI-Cray; Dr. Ambuj Goyal, Vice President, Systems and Software, IBM Research Division; Professor George Milne, University of South Australia; Dr. Kenichi Miura, High Performance Computing Group, Fujitsu Limited; and Professor Shuichi Sakai, University of Tsukuba. The titles of their keynote addresses are, respectively, Microprocessor System Performance: Beyond the MHz and SPECint Hype; Trends in High Performance Computing; The Promise of Information Technology; Reconfigurable Computing as a Supercomputer Replacement; Vector-Parallel Processing Approach to High Performance Computing; and Seamless Computing: Integrating Parallel Computing and Network Computing with Future PCs.

The panel is titled Network Computing: Evolution or Revolution and the panelists are Professor Arvind (moderator), Professor Anant Agrawal, Dr. Dileep Bhandarkar, Dr. Ambuj Goyal, Professor George Milne, Dr. N. Radhakrishnan, Professor S.V. Raghavan, and Professor Shuichi Sakai.

Finally, the tutorials are: Programming Parallel Computers for High Performance (Professors J. Ramanujam and P. Saddayapan); Emerging Internet Technologies and Protocols (Professor Guru Parulkar); Mobile and Wireless Computing (Dr. Prathima Agrawal); Performance Modeling of High-Performance Systems (Professors A.J.C. van Gemund and S.K. Nandy); Internet, Java, and Java Computing in Practice (Dr. Rajkumar); High Performance Networked Multimedia Systems (Professor Guru Parulkar); and Understanding ATM Networks (Dr. Saragur Srinidhi).

A conference of this magnitude cannot be organized without the tireless effort of many individuals. I’d like to thank all the conference volunteers for their enormous contribution towards the success of this conference. In particular, I wish to thank the conference co-chairs (Professors Prasanna and Balakrishnan), the program vice-chairs (Professors Mary Jane Irwin, Kai Hwang, Jose Rolim, Bhabani Sinha, and Albert Zomaya), the program committee, the expert referees, the authors of all contributed papers, the presenters of accepted papers, the keynote speakers, the panelists, the session chairs, the tutorial lecturers, Professor Arvind who organized the panel and the keynote addresses, Professor Govindarajan who organized the special session and mini panel on instruction-level parallelism, Professor Banerjee who organized the poster...
session, Dr. D.N. Jayasimha who organized the tutorials, and Professor S.K. Nandy who organized the industrial track.

Sartaj Sahni
University of Florida