Welcome

Welcome to EURO-DAC’96 with EURO-VHDL’96.

As we welcome you all to EURO-DAC’96 with EURO-VHDL’96 in Geneva, the fifth and premier event to support the European Electronic Design Automation Industry, we are realizing our objective of blending the latest scientific research with exhibits which display the best EDA-products worldwide with a User Forum for disseminating practical application experience.

The event gives the european professional greater access to the information needed to perform and excel in the fast pace world of Electronic Design.

As we enter the era of deep submicron design and complete systems on silicon new challenges are emerging for design automation if it is to stay in step with the rapid advances in implementation technology. These range from new modelling methods through more comprehensive analysis techniques; the merging of disparate design disciplines to new means to manage the volume of data and the design process. While designing at higher and higher levels of abstraction in order to deal with complexity is becoming the norm stronger links at all stages between specification and implementation are required in order to ensure finer geometries and faster clocking speeds from the manufactured devices.

But that is what makes our industry so exciting; over the whole range, from leading edge theory to user experience, the event will comprise panel sessions, tutorials, keynote talks and vendor sessions designed to stimulate lively discussion. At EURO-DAC ’96 with EURO-VHDL ’96 theory and practice ideally combine through the overall concept of conference and exhibition to progress the industry it serves.

Graham Symonds, General Chair’96
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