IEEE Computer Society

TTTC: Test Technology Technical Council

TTTC IN GENERAL

PURPOSE: The Test Technology Technical Council is a volunteer professional organization sponsored by the IEEE Computer Society. The goals of TTTC are to contribute to members' professional development and advancement and to help them solve engineering problems in electronic test, and help advance the state-of-the-art. In particular, TTTC aims at facilitating the knowledge flow in an integrated manner, to ensure overall quality in terms of technical excellence, fairness, openness, and equal opportunities.

MEMBERSHIP: Membership is open to all individuals interested in test engineering at a professional level.

DUES: There are NO dues for TTTC membership and no parent-organization membership requirements.

BENEFITS: The TTTC members benefit from personal association with other test professionals. They may have the opportunity to be involved on a wide range of committees. They receive appropriate and updated information and announcements. There are substantial reductions in fees for TTTC-sponsored meetings and tutorials for members of IEEE and/or IEEE Computer Society.

TTTC ACTIVITIES

TECHNICAL MEETINGS: To spread technical knowledge and advance the state-of-the-art, TTTC sponsors many well-known conferences and symposia and holds numerous regional and topical workshops worldwide.

STANDARDS: TTTC initiates, nurtures and encourages new test standards. TTTC-initiated Working Groups have produced numerous IEEE standards, including the 1149 series used throughout the industry.

TECHNICAL ACTIVITIES: TTTC sponsors a number of Technical Activity Committees (TACs) that address emerging test technology topics and guide a wide range of activities.

TUTORIALS and EDUCATION: TTTC sponsors a comprehensive Test Technology Educational Program (TTEP). This program provides opportunities for design and test professionals to update and expand their knowledge base in test technology, and to earn official accreditation from IEEE TTTC, upon the completion of four full day tutorials proposed by TTEP.

TTTC CONTACT

TTTC On-Line: The TTTC Web Site at http://tab.computer.org/tttc offers samples of the TTTC Newsletter, information about technical activities, conferences, workshops and standards, and links to the Web pages of a number of TTTC-sponsored technical meetings.

Becoming a MEMBER: Becoming a TTTC member is extremely simple. You may either contact by phone or e-mail the TTTC office, or fill out and submit a TTTC application form, or visit the membership section of the TTTC web site.

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TTTC-Sponsored Technical Meetings in 2009
For the most current information, please visit the TTTC website (http://tab.computer.org/tttc) or TTTC Events website (http://www.tttc-events.org)

3/2-3/5  Latin American Test Workshop (LATW), Rio de Janeiro, Brazil  M. Lubaszewski, Y. Zorian
3/23-3/25  International Test Synthesis Workshop (ITSW), Austin, TX, USA  J. Dworak
3/24-3/25  Workshop on Silicon Errors in Logic - System Effects (SESEL), Stanford, CA, USA  A. Wood
4/15-4/17  Design & Diagnosis of Electronic Circuits & Systems Workshop (DDECS), Liberec, Czech Republic  H. Manhaeve
4/20-4/24  Design, Automation and Test in Europe (DATE), Nice, France  L. Benini
5/3  Workshop on Test of Wireless Circuits and Systems (WTW), Santa Cruz, CA, USA  R. Akken
5/3-5/7  VLSI Test Symposium (VTS), Santa Cruz, CA, USA  M. Abadir
5/12-5/15  Workshop on Signal Propagation on Interconnects (SPI), Strasbourg, France  D. Deschacht
5/25-5/29  European Test Symposium (ETS), Sevilla, Spain  J.L. Huertas Diaz
6/10-6/12  Int'l Mixed-Signals, Sensors, and Systems Test Workshop (IMSSTW), Scottsdale, Arizona  S. Ozev
7/26  Int'l Workshop on Design for Manufacturability & Yield (DFM&Y), San Francisco, CA, USA  Y. Zorian
7/27  Int'l Workshop on Hardware-Oriented Security and Trust (HOST), San Francisco, CA, USA  M. Tehranipoor
8/31-9/2  Int'l Workshop on Memory Technology, Design and Testing (MTDT), Hsinchu, Taiwan  C.-W. Wu, R. Rajsuman
9/11-9/14  East-West Design and Test Symposium (EWDTS), Moscow, Russia  V. Hahanov, Y. Zorian
9/15-9/17  Board Test Workshop (BTW), Fort Collins, CO, USA  W. Eklow
10/7-10/9  Int'l Symposium on Defect & Fault Tolerance in VLSI Systems (DFTS), Chicago, IL, USA  M. Tehranipoor, D. Gizopoulos
11/1-11/6  International Test Conference (ITC), Austin, TX, USA  G. Roberts
11/5-11/6  Int'l Workshop on Design for Reliability and Variability (DRV), Austin, TX, USA  M. Nicolaidis, Y. Zorian
11/5-11/6  Int'l Workshop on Test and Validation of High-Speed Analog Circuits (TVHAC), Austin, TX  A. Majumdar, V. Ganti
11/5-11/6  Int'l Workshop on Defect and Data Driven Testing (D3T), Austin, TX, USA  M. Tehranipoor
11/5-11/6  Int'l High Level Design Validation and Test Workshop (HLDVT), San Francisco, CA, USA  P. Kalla
11/15-11/17  International Design & Test Workshop (IDT), Riyadh, Saudi Arabia  S. Al Humaidi
11/23-11/26  Asian Test Symposium (ATS), Taichung, Taiwan  S.-Y. Huang, M.-D. Shieh
11/27-11/28  Workshop on RTL and High Level Testing (WRTL), Hong Kong, China  K. Chakrabarty
12/2-12/3  Int'l Workshop on Testing Embedded and Core-Based System-Chips (TECS), Online  Y. Zorian
12/7-12/9  International Workshop on Microprocessor Test and Verification (MTV), Austin, TX, USA  M. Abadir

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