Foreword to the 15th IEEE DDECS Symposium

We cordially welcome all the participants of DDECS 2012 in Tallinn. DDECS has been organized as a regional Central and East-European workshop until 2008, in 2009 it was upgraded to a symposium. Before 2012, it has been held in the Czech Republic (1995, 2002, 2006, 2009), Hungary (2001, 2005), Poland (1997, 2003, 2007), Slovakia (2000, 2004, 2008), Austria (2010) and Germany (2011). After the very successful event in Cottbus, the symposium has moved to the Baltic States for the first time.

DDECS 2012 will take place in Tallinn, the capital of Estonia, whose picturesque Old Town with its fascinating medieval architecture and slender church spires is included in the UNESCO World Heritage list. Being a former Soviet republic, Estonia is nowadays tightly integrated to the world community as a member of EU, Schengen treaty, Euro currency and NATO. The tiny country of just 1.3 million people is sometimes dubbed e-stonia due to its advanced IT infrastructure.

DDECS symposium has become one of the major scientific events in the area of design and test in Central Europe and, as in previous years, it is sponsored by the IEEE Computer Society and Test Technology Technical Council (TTTC). Tallinn University of Technology, in particular the Computer Engineering Department in the Faculty of Information Technology, Estonian ICT Doctoral School and Testonica Lab as the corporate sponsor of DDECS 2012 jointly support the organization of the symposium this year.

It is our pleasure to report that this year we received 130 submissions from virtually all over the world. Thanks to the precious work of the Program Committee members and reviewers, more than 400 reviews were completed that contributed to the high quality of the technical program consisting of 48 oral and 30 poster presentations. The program includes three keynote speeches targeting the emerging trends and technologies in design and test of electronic circuits and systems. In addition, four embedded tutorials will be presented at the symposium.

Starting from 2004, the DDECS Steering Committee (SC) started awarding the Jan Hlavička prize to the best papers presented at the DDECS symposium – one paper from the design field and one paper from the test field. The best papers are selected by a special committee comprising SC members and DDECS symposium chairs. The committee takes into consideration the papers with the best ranking based on the evaluation by reviewers, quality of their publication in the proceedings and author’s presentation. From DDECS 2011, two outstanding contributions were selected as best papers:

- Elena Ioana Vatajelu, Joan Figueras, UPC, Spain, Statistical Analysis of 6T SRAM Data Retention Voltage under Process Variation.
- Matthias Sauer, Alexander Czutro, Tobias Schubert, Stefan Hillebrecht, Ilia Polian, Bernd Becker, Univ. of Freiburg and Univ. of Passau, SAT-based analysis of sensitisable paths.

As in the previous year, the best papers will be submitted to IEEE Design and Test Magazine.

We wish the symposium participants interesting presentations and the opportunity to refresh contacts and establish new ones and to find ideas for future co-operation. Furthermore, we wish all the visitors a pleasant and inspiring atmosphere during the 15th IEEE DDECS Symposium 2012.

Welcome to the IEEE DDECS Symposium in Tallinn!

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