PANEL SESSION – Great Challenges in Nanoelectronics and Impact on Academic Research: More than Moore or beyond CMOS?

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This panel session will address the post-CMOS research great challenges and opportunities corresponding to the More Than Moore and Beyond CMOS domains. The opening talk of the panel will set the ground for discussion with some key examples placed at the intersection of the two domains. Especially the role of functional diversification and of new research and application drivers, different from scaling, will be critically discussed by a team of high-level experts in the field. Moreover, the impact of post-CMOS era on the way the academic research and the education of engineering are conceived today and should be adapted in the future will be the center of the debate.