2001 IEEE Fellows

• Jason Cong - University of California, Los Angeles, CA
  For contributions to the computer-aided design of integrated circuits, especially in physical design automation, interconnect optimization, and synthesis of field-programmable gate-arrays.

• Massoud Pedram - University of Southern California, Los Angeles, CA
  For contributions to the theory and practice of low-power design and CAD.

CAD Transactions Best Paper Award

• Shigeru Yamashita - NTT Communication Science Laboratories, Kyoto, Japan
  Hiroshi Sawada - NTT Communication Science Laboratories, Kyoto, Japan
  Akira Nagoya - NTT Network Innovation Laboratories, Kanagawa, Japan
  For the paper:
  “SPFD: A New Method to Express Functional Flexibility”
  *IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems*,
  vol. 19, no. 8, pp. 840-849, August 2000.

VLSI Transactions Best Paper Award

• Rajamohana Hegde - Univ. of Illinois, Urbana, IL
  Naresh R. Shanbhag - Univ. of Illinois, Urbana, IL
  For the paper:
  *IEEE Transactions on Very Large Scale Integration (VLSI) Systems*,
  vol. 8, no. 4, pp. 379-391, August 2000.

Outstanding Young Author Award

• Manish Pandey - Silicon Access Networks, Inc., San Jose, CA
  For the paper:
  “Exploiting Symmetry when Verifying Transistor-Level Circuits by Symbolic Trajectory Evaluation,”
  *IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems*,

CAS Industrial Pioneer Award

• Aart de Geus - Chairman and CEO, Synopsys, Inc., Mountain View, CA
  For pioneering logic synthesis.

Marie R. Pistilli Women in EDA Achievement Award

• Deirdre Hanford - Senior VP, Business & Market Development, Synopsys, Inc., Mountain View, CA
  For her significant contributions in helping women advance in the field of DAC technology.