Design Automation Conference Graduate Scholarships

Each year the Design Automation Conference sponsors several $12,000 scholarships to support graduate research and study in Design Automation (DA), with emphasis in "design and test automation of electronic and computer systems". Each scholarship is awarded directly to a university for the Faculty Investigator to expend in direct support of one or more DA graduate students.

The criteria for granting such a scholarship expanded in 1996 to include financial need. The criteria are: the academic credentials of the student(s); the quality and applicability of the proposed research; the impact of the award on the DA program at the institution; and financial need. Preference is given to institutions that are trying to establish new DA research programs.

Information on next year's DAC scholarship award program will be available on the DAC World Wide Web page at: http://www.dac.com/scholarship.html.

Design Automation Conference Graduate Scholarship Awards

• Prof. R. Iris Bahar of the Brown University, Providence, RI, for Qian Cui. Their project is entitled, "Using Implications to Drive Low-Power Optimization of Technology-Dependent Circuits".

• Prof. David Kaeli of the Northeastern University, Boston, MA, for Ying Liu. Their project is entitled, "Designing in the 3rd Dimension".

• Prof. Miodrag Potkonjak of the University of California, Los Angeles, CA, for Inki Hong. Their project is entitled, "Design Methodology for Synthesis of Real-Time Systems on Silicon".

• Prof. Shambhu J. Upadhyaya of the State University of New York, Buffalo, NY, for Kamran Zarrineh. Their project is entitled, "A Design for Test Perspective on Memory Synthesis".

The Lucent Technologies supported DAC Graduate Scholarship is awarded to:

• Prof. Sachin S. Sapatnekar of the Iowa State University, Ames, IA, for Naresh Maheshwari and Yanbin Jiang. Their project is entitled, "Practical Methods for Gate-Level and Transistor-Level Timing Optimization".

The Xilinx supported DAC Graduate Scholarship is awarded to:

• Prof. Michael J. Alexander of the Washington State University, Pullman, WA, for Yongmin Ge. Their project is entitled, "CAD-Driven FPGA Architecture Design".

Design Automation Conference Graduate Scholarship Committee

The 1997 DAC Scholarship Committee was comprised of the following people:

James P. Cohoon, University of Virginia (Chair)
Philip V. Lopresti, Independent Consultant
Jeffrey S. Salowe, Cadence Design Systems, Inc.

1997 SIGDA Meritorious Service Award

Robert A. Walker
Kent State University
Kent, OH