Tributes to Prof. McCluskey

Pioneer in reliable/dependable digital electronic systems

Masahiro Fujita
University of Tokyo, Japan

Masahiro Fujita received his Ph.D. in Information Engineering from the University of Tokyo in 1985 on his work on model checking of hardware designs. From 1993 to 2000, he was director at Fujitsu Laboratories of America and headed a hardware formal verification group. Since March 2000, he has been a professor at VLSI Design and Education Center of the University of Tokyo. He has done innovative work in the areas of hardware verification, synthesis, testing, and software verification-mostly targeting embedded software and web-based programs. He has authored and co-authored 10 books, and has more than 300 publications. He has been involved as program and steering committee member in many prestigious conferences on CAD, VLSI designs, software engineering, and more. His current research interests include synthesis and verification in SoC (System on Chip), hardware/software co-designs targeting embedded systems, digital/analog co-designs, and formal analysis, verification, and synthesis of cyber physical systems.

What I have learned from Professor EdMcCluskey

Adit D. Singh
Auburn University, USA

Adit D. Singh is James B. Davis Professor of Electrical and Computer Engineering at Auburn University, USA. Before joining Auburn in 1991, he served on the faculty at the University of Massachusetts in Amherst, and Virginia Tech in Blacksburg. His technical interests span all aspects of VLSI technology, in particular integrated circuit test and reliability. He has published over two hundred research papers, lectured widely all over the world, and served as a consultant to several semiconductor companies. He has also served as an expert witness in major patent litigation cases, and is himself a co-inventor on international patents that have been licensed to industry. He has had leadership roles as General Chair/Co-Chair/Program Chair for dozens of international VLSI design and test conferences and also on the editorial boards of several journals, including IEEE Design and Test and JETTA. Singh is a past Chair of the IEEE Test Technology Technical Council (TTTC), and has also served on the Board of Governors of the IEEE Council on Design Automation (CEDA). He is a Fellow of IEEE and a Golden Core member of the IEEE Computer Society.
Professor McCluskey, a great researcher and educator

James Chien-Mo Li
National Taiwan University, Taiwan

James Chien-Mo Li received his BSEE degree in 1993 from National Taiwan University, Taipei, Taiwan. He received his MSEE and PhD degrees in electrical engineering from Stanford University in 1997 and 2002 respectively. His PhD advisor is Professor McCluskey. He is currently a professor of Graduate Institute of Electronics Engineering, National Taiwan University, Taipei, Taiwan. His research interest includes test generation, low power testing, and diagnosis. He has coauthored three books in EDA and testing.

Learning how to teach

Hans-Joachim Wunderlich
University of Stuttgart, Germany

Hans-Joachim Wunderlich is a full professor and the director of the Institute of Computer Architecture and Computer Engineering at the University of Stuttgart. He studied Mathematics and Philosophy at the Universities of Konstanz and Freiburg, Germany, and received his PhD degree (Dr. rer. nat.) from the University of Karlsruhe in 1986. Since then, he has worked as a professor at the universities of Karlsruhe, Duisburg, Siegen and Stuttgart.

For more than 32 years, Prof. Wunderlich contributed to the areas of VLSI testing, Design for Test, Dependability, Fault Tolerance and Design Automation. He published more than 250 books and articles in these fields, and led numerous projects funded by industry, European Commission, German government or national funding organizations like DFG. Prof. Wunderlich was recipient of the award for excellent academic teaching of the state of Baden-Württemberg, was promoted Golden Core Member of the IEEE Computer Society, and was elevated Fellow of the IEEE.