Invited Talk I

Invited Talk by Yervant Zorian

Speaker’s Bio

Dr. Yervant Zorian is a Chief Architect and Fellow at Synopsys, as well as President of Synopsys Armenia. Formerly, he was Vice President and Chief Scientist of Virage Logic, Chief Technologist at LogicVision, and a Distinguished Member of Technical Staff AT&T Bell Laboratories. He is currently the President of IEEE Test Technology Technical Council (TTTC), the founder and chair of the IEEE 1500 Standardization Working Group, the Editor-in-Chief Emeritus of the IEEE Design and Test of Computers and an Adjunct Professor at University of British Columbia. He served on the Board of Governors of Computer Society and CEDA, was the Vice President of IEEE Computer Society, and the General Chair of the 50th Design Automation Conference (DAC) and several other symposia and workshops.

Dr. Zorian holds 35 US patents, has authored four books, published over 350 refereed papers and received numerous best paper awards. A Fellow of the IEEE since 1999, Dr. Zorian was the 2005 recipient of the prestigious Industrial Pioneer Award for his contribution to BIST, and the 2006 recipient of the IEEE Hans Karlsson Award for diplomacy. He received the IEEE Distinguished Services Award for leading the TTTC, the IEEE Meritorious Award for outstanding contributions to EDA, and in 2014, the Republic of Armenia's National Medal of Science.

He received an MS degree in Computer Engineering from University of Southern California, a PhD in Electrical Engineering from McGill University, and an MBA from Wharton School of Business, University of Pennsylvania.
Secure Value Chain Enablement with Smart-Connected SoCs

Michael Chen
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Abstract
Mentor Graphics is developing a secure end-to-end platform that enables SoC Suppliers to establish inborn hardware root-of-trust, and provide anti-reverse engineering protection. Solution creates unique fingerprint on every die, securely register Chip ID’s into operations server and authenticate SoCs in the supply chain to monitor usage, securely configure IPs, enabling multi-debug modes, and establishing layered security. As a result, connected SoC suppliers will be able to provision SoC features and SKUs anywhere in the value chain and can gather analytics on field use to reduce field failures and provide secure field updates.

Speaker’s Bio
A twenty-five year veteran at Mentor Graphics, Michael Chen is currently a Business Unit Director in the New Ventures Division. As such, Michael manages leading-edge technology efforts for the company’s Design for Security platform. Michael is invited often to talk about designing security and secure IC supply chain. He has presented at conferences such as GOMACTech, IEEE VLSI Test Symposium, CHASE (Center for Hardware Assurance, Security, & Engineering) conference, SEMICOn West, and the Design Automation Conference where he chaired a special session on “Who is the biggest threat to tomorrow’s security?” He currently serves as Chair of the Trustworthy and Secure Semiconductors and Systems (T3S) TAB committee with Semiconductor Research Corporation (SRC) and collaboration with National Science Foundation. He holds several patents.