
This year's Symposium is hosted by the University of California at Berkeley. UC Berkeley is a hub of academic excellence and entrepreneurship. It is renowned worldwide for the scope of its research and publications, and the quality of its libraries. The campus is an urban oasis with architectural and historic landmarks, landscaped around Strawberry Creek. We anticipate that Berkeley's beautiful setting and the notoriously open mindset of its faculty and students create the perfect environment to discuss advances, next steps and totally new directions for asynchronous technologies. Berkeley is also a gateway to Silicon Valley, which provides many opportunities to engage countless companies in need or in charge of new technology solutions for VLSI circuits and systems.

We received 53 paper submissions, of which we accepted 18 papers for final publication and presentation at the symposium. The papers were rigorously reviewed by a Technical Program Committee (TPC) of 40 reviewers, with most papers receiving 5 reviews. In some cases, additional reviews were solicited from experts outside this Committee to help finalize decisions.

As has been the trend in recent years, ASYNC 2007 received a large number of high-quality papers addressing system-level timing, interfacing, and communication. Eight of these have been included in the final program. The remaining accepted papers cover a wide range of topics, from novel designs and architectures to on-chip test and measurements, synthesis, and formal methods for modeling and verification.

Similar to last year, the Best Paper Award has been determined in advance by a dedicated subcommittee of TPC and guest reviewers. This subcommittee ranked the camera-ready papers to select an agreed-upon final subset for Best Paper consideration. They carefully read and discussed all papers in this subset in depth, and selected a winner. The Best Paper will be announced and awarded at the Symposium.

To complement the technical program we have four distinguished speakers, who will add color and variety. Jim Kajiya from Microsoft will talk about the relationship between charge and flux in traditional CMOS and superconducting circuits. Dr. Kajiya is engaged in exploring the future of computer architectures. Carlo Sequin from UC Berkeley will describe some of his recent work in abstract geometric sculptures. Dr. Sequin puzzles about how to exhibit the symmetry of multi-dimensional geometric shapes and topologies. At the banquet, we will have a profusely illustrated talk by Steve Jacobsen about some of his robots. Dr. Jacobsen, is the founder of Sarcos, Inc and has close ties with the University of Utah. His works range from micromechanical sensors, artificial limbs, the Bellagio fountains, the Jurassic Park dinosaurs, and many other spectacular robot devices. Kevin Nowka from IBM Research closes the ranks, and will inevitably add variation when he leads us into the future world of semiconductor processing.

Once again, we enjoyed generous contributions from industry. Please refer to the inside cover to these Proceedings or the conference web page for the complete list of our sponsors. Their financial support greatly enhances the value of ASYNC 2007 by providing registration and travel grants for students, so more students can attend and interact with experts in the field and with companies that commercialize the technology. This year we give special attention to innovation and commercialization through a technical demo session on Monday afternoon, where academia and industry show new experiments and advances in asynchronous technology.
ASYNC 2007 was made possible thanks to the help of many individuals. We would first like to thank all authors for submitting the high quality papers that form the foundation of this symposium. We thank all reviewers for their dedication and the time they voluntarily set aside to review, discuss and select the best ASYNC 2007 paper submissions and for providing detailed feedback to the authors to improve their draft papers. We thank IEEE Computer Society, in particular Elvia Gonzalez, Nikki Nasabzadeh, Stacey Wagner and Jim Harvey, for their excellent support in working out contracts, budgets and registration specifics. We also thank Silvia Ceballos for managing the camera-ready papers and the editorial production of these Proceedings. Our special thanks go to Richard Newton, professor and dean of the College of Engineering at the University of California, Berkeley, who generously gave us access to the Bechtel Engineering Center and the Hearst Memorial Mining Building for hosting ASYNC 2007. Dr. Newton passed away on the 2nd of January, 2007; he will be with us in our hearts and minds when we welcome you to UC Berkeley. Our final thanks go to the people of UC Berkeley, in particular William Oman, Portia Groce and Tamera Garlock, the people of the University of Southern California, specifically Arash Saifhashemi, the people of Sun Microsystems, and numerous students from UC Berkeley and the University of Southern California for providing the local arrangements that make ASYNC 2007 a productive and enjoyable experience.

We wish you an exiting symposium and a memorable visit to Berkeley and the San Francisco Bay Area.

Peter Beerel and Marly Roncken
General Co-Chairs

Mark Greenstreet and Montek Singh
Program Co-Chairs

Ken Stevens and Pascal Vivet
Industrial Co-Chairs

Jan Rabaey and Ivan Sutherland
Invited Speakers Co-Chairs

Peter Hofstee
Best Paper Award Chair

Yaeko Hirotsuka, Jeff Rulifson, and Jeanie Treichel
Local Arrangements Co-Chairs