The two important issues surrounding system LSI designs are large development costs and long development schedules. This session will introduce leading-edge reconfigurable device technologies that can address these issues as well as offer a glimpse of innovative technologies into the future.

Traditionally, ASICs such as gate arrays and standard cells have been employed to develop system LSIs. FPGAs offer a dramatic reduction in development cost and schedules for such complex system LSI designs. Today’s FPGA not only offers a high density and performance platform, but it allows reconfiguration of functions and other benefits, such as:

- dynamically reconfigurability,
- embedding a processor and a reconfigurable logic together,
- automatic command set generation, etc.

In this panel, several new promising architectures for computing systems will be introduced and other new applications will be discussed.

Professor Satoshi Goto of Waseda University will chair this panel. Panelists will include representation from NEC, Xilinx, IP Flex, QuickSilver and IMEC.