Technical Program Co-Chairs’ Message

On behalf of the Technical Program Committee of ASP-DAC 2000, I would like to welcome all of you to ASP-DAC in the memorial year of 2000. Following the past successful conferences in Tokyo (1995, 1997, 1998) and in Hong Kong (1999), the Committee has invited contributions for 2000 conference in the area of Design Automation of various design stages and LSI design methodologies.

This year, our committee specifically introduced LSI Development into the area of interest for the ASP-DAC 2000 Technical Program. The number of gates on a chip is rapidly approaching 10 million gates. System-On-A-Chip is now in our hands. As the roles of CAD tools in these highly complex system designs become more and more important, CAD tool designers have to pay particularly close attention to the latest system design examples and methodologies. In this context, we believe that our ASP-DAC needs to increase presentations of LSI developments, to provide a place where system designers and CAD designers can exchange ideas and establish an intensive and mutual understandings of the important problems we are currently facing.

Also, reflecting the current trend of System-On-A-Chip, there is a great increase in paper submissions related to system-level design automation which includes software/hardware co-design, a variety of system-level optimization issues, system description languages, etc. Physical layout CAD continues to provide challenging new topics and maintains to be one of the major contributors to this conference. Especially, continuous efforts on conquering various layout problems in deep submicron process have been undertaken by many researchers.

For this conference, 144 technical papers were submitted from 14 countries. The technical program committee have reviewed all the submitted papers and accepted 80 regular papers and 10 short papers for presentation at the conference. We have organized the session tracks based on the field categories, each of which runs in the same room. They are 1) LSI Design Track, 2) System DA track, 3) Logic DA track including testing, 4) Physical DA track, 5) Device CAD track including Analog DA. They are designed so that the participants can easily follow the session tracks related to their own fields.

There are also 8 special sessions and panel sessions included in our technical program, inviting a number of distinguished researchers from around the world. These sessions address a wide variety of important issues such as CAD for embedded systems, System-In-Packaging (SIP), timing closure between logic and physical designs, issues on industry-academia cooperation, and system level design languages. Also, there are a number of embedded tutorials and invited talks within the regular sessions. We would like to express our appreciations for the efforts of organizers, chairs, panelists and invited speakers, which add special values to our program.

Best paper awards are presented to the authors whose papers are of exceptionally high quality among all the presented papers. 7 best paper award candidates were first nominated by the Technical Program Committee, where they were further subjected to intensive evaluation by the Committee.

As co-chair of ASP-DAC 2000 Technical Program Committee, I am really grateful for all of the members of our committee, organizers of special sessions and panel sessions, chairs and speakers of special sessions and regular sessions for their continuing efforts. Among them, I really appreciate TPC secretaries, Dr. Kazuhiro Ito, Dr. Kei Suzuki and Dr. Tsuyoshi Ishikawa for their dedications. Without them, ASP-DAC 2000 could not be held. At last, I would like to emphasis that the real success depends on how much all of you, the participants of ASP-DAC 2000, show your attention and join the conference activity. In other words, I hope you can enjoy 3 days hot discussions through this conference. Please join us.

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