University LSI Design Contest Summary

Eight designs were submitted from three countries, Japan, Korea, and Indonesia. Statistics in terms of the application areas, and design methodologies are summarized as follows:

<table>
<thead>
<tr>
<th>Country</th>
<th>Number of Submission</th>
<th>Application Area</th>
<th>Design Methodology</th>
</tr>
</thead>
<tbody>
<tr>
<td>Japan</td>
<td>2</td>
<td>A: 0, D: 1, M: 1, C: 0</td>
<td>F/C: 2, G/A: 0</td>
</tr>
<tr>
<td>Korea</td>
<td>5</td>
<td>A: 3, D: 1, M: 1, C: 0</td>
<td>F/C: 4, G/A: 1</td>
</tr>
<tr>
<td>Indonesia</td>
<td>1</td>
<td>A: 0, D: 0, M: 0, C: 1</td>
<td>F/C: 1, G/A: 0</td>
</tr>
</tbody>
</table>

A: Analog or A/D Mixed Signal; D: DSP/Multimedia Application; M: Microprocessor; C: Custom Application; F/C: Full Custom or Cell-based; G/A: Gate Array

The submitted designs were reviewed by the members of the Design Contest Committee. After the review, seven designs were selected and one design was rejected. From the selected designs, the Design Contest Committee decided to confer an Outstanding Design Award and a Special Feature Award to two specific designs, which are summarized as follows.

**Outstanding Design Award**

“A 16-bit DSP and System for Baseband/Voiceband Processing of IS-136 Cellular Telephony”

Tae Hun Kim, Jeongsik Yang, Kyoo Hyun Lim, Jin Wook Kim, Jeong Eun Lee, Hyoung Sik Nam, Young Gon Kim, Jeong Pyo Kim, Sang Jin Byun, Bae Sung Kwon, and Beomsup Kim

Department of Electrical Engineering, Korea Advanced Institute of Science and Technology, Taegon, Korea

(Digital Signal Processor, Full Custom Design)

**Special Feature Award**

“Motion Estimator LSI for MPEG2 High Level Standard”

Li, Jiang, Dongju Li, Shintaro Haba, Chawalit Honsawek, and Hiroaki Kunieda

Department of Electrical and Electronics Engineering, Tokyo Institute of Technology, Tokyo, Japan

(Digital Signal Processor, Full Custom Design)