Message from the ASAP ’09 General and Technical Program Chairs

Welcome to ASAP 2009: the 20th IEEE International Conference on Application-specific Systems, Architectures and Processors. This year’s event takes place in Boston, Massachusetts, USA. The history of the event traces back to the International Workshop on Systolic Arrays, organized in 1986 in Oxford, UK. It later developed into the International Conference on Application Specific Array Processors. With its current title, it was organized for the first time in Chicago, Illinois, USA in 1996. Since then it has alternated between Europe and North America.

In this, its 20th incarnation, ASAP is a wonderful conference that can boast of a long history and a vibrant, relevant present and future. Out of 70 manuscript submissions, a strong program of 25 regular papers and 12 poster papers was accepted for presentation at the conference. The session titles summarize the variety of topics covered:

- Arithmetic
- FPGA Applications I
- Media and Image Processing
- FPGA Application II
- Arithmetic and Cryptography
- Application-specific Integrated Circuits
- Computational Biology
- Tools and Design Aids
- Application-specific Instruction Processors

We would like to thank the all the authors who responded to the call for papers. It is your efforts that make the conference what it is. We would also like to thank the keynote speakers and panelists. A special thank you to the entire Program Committee as well as the external reviewers for a job well done. We are grateful to our sponsors – the IEEE Computer Society, Mercury Computer Systems, and The Mathworks. We particularly thank Xinming Huang, Wei Qin, and Tim Leonard for their tireless efforts on behalf of the conference.

This year’s conference is an excellent example of the dissemination of important research results, and we are all proud to be a part of it.

General Chairs: Miriam Leeser and Martin Herbordt

Technical Program Chairs: Jeremy Buhler, Roger D. Chamberlain, Milos Ercegovac, and Mark Franklin