Message from the Program Chairs

The IEEE Application-Specific Array Processors conference provides a well-established forum for the presentation and discussion of problems in the field of parallel array algorithms and architectures, of software tools dedicated to special-purpose systems, and of important applications suitable for execution in an application-specific manner.

This year's program covers a balanced mix of the above topics. Scheduling and mapping techniques, design methodologies, array architectures, computer arithmetic, signal and image processing applications, motion estimation, are the main areas of interest.

The reviewing of the papers was handled by program committee members and external reviewers. Each paper was reviewed by three reviewers solicited by the program chairs. Additional reviews were carried out on papers receiving conflicting reviews at the Program Committee meeting on March 1st, 1995 at ENS Lyon, France.

In addition to the contributed papers (full, short and posters), the conference features a keynote address by Prof. Lothar Thiele of ETH Zurich on "Array Processing in Embedded Systems." We have also included two invited talks on "Real-time Image Processing for Multimedia Applications at Matra-Hachette" by Dr. Philippe Isambert of Matra Cap Systemes (Paris) and on "Computer Arithmetic: Exploiting Redundancy in Number Representations" by Peter Kornerup of Odense University.

On behalf of the Program Committee, we would like to thanks the authors for their contributions. We thank the program committee members and the reviewers for their careful evaluation of all the papers submitted. Finally, we thank the conference chairs for their close collaboration, and the steering committee members, Earl Swartzlander, Jose Fortes, and S.Y. Kung, for inviting us to chair the Program Committee.

Patrice Quinton and Yves Robert