

IEEE MICRO

2006 ANNUAL INDEX, VOL. 26

AUTHOR INDEX

A

- Abel, F., see Minkenberg, C., May-June, 2006, pp. 58-71.
- Agarwal, A., S. Mukhopadhyay, A. Raychowdhury, K. Roy, and C.H. Kim. "Leakage power analysis and reduction for nanoscale circuits," March-April, 2006, pp. 68-80.
- Ailamaki, A., see Wenisch, T.F., July-Aug., 2006, pp. 18-31.
- Akkary, H., see Gandhi, A., Jan.-Feb., 2006, pp. 30-39.
- Alameldeen, A.R., and D.A. Wood. "IPC considered harmful for multiprocessor workloads," July-Aug., 2006, pp. 8-17.
- Alastruey, J., J.L. Briz, P. Ibanez, and V. Vinals. "Software demand, hardware supply," July-Aug., 2006, pp. 72-82.
- Amit Kumar, see Shang, L., Jan.-Feb., 2006, pp. 130-139.
- Ananian, C.S., K. Asanovic, B.C. Kuszmaul, C.E. Leiserson, and S. Lie. "Unbounded transaction memory," Jan.-Feb., 2006, pp. 59-69.
- Andrews, J., and N. Baker. "Xbox 360 system architecture," March-April, 2006, pp. 25-37.
- Asanovic, K., see Ananian, C.S., Jan.-Feb., 2006, pp. 59-69.

B

- Bahar, R.I., see Nepal, K., Sept.-Oct., 2006, pp. 19-27.
- Baker, N., see Andrews, J., March-April, 2006, pp. 25-37.
- Balaji, P., Wu-chun Feng, and D.K. Panda. "Bridging the Ethernet-Ethernot performance gap," May-June, 2006, pp. 24-40.
- Balasubramonian, R., N. Muralimanohar, K. Ramani, Liqun Cheng, and J.B. Carter. "Leveraging wire properties at the microarchitecture level," Nov.-Dec., 2006, pp. 40-52.
- Barnes, R.D., S. Ryoo, and W.-m.W. Hwu. "Tolerating cache-miss latency with multipass pipelines," Jan.-Feb., 2006, pp. 40-47.
- Berry, H., see Gracia Perez, D., Nov.-Dec., 2006, pp. 14-28.
- Bhuyan, L.N., see Li Zhao, May-June, 2006, pp. 72-84.
- Binkert, N.L., R.G. Dreslinski, L.R. Hsu, K.T. Lim, A.G. Saidu, and S.K. Reinhardt. "The M5 simulator: Modeling networked systems," July-Aug., 2006, pp. 52-60.

- Bowman, K., see Unsal, O.S., Nov.-Dec., 2006, pp. 30-39.
- Brightwell, R., see Petrini, F., May-June, 2006, pp. 7-9.
- Brightwell, R., K.T. Predretti, K.D. Underwood, and T. Hudson. "Seastar interconnect: Balanced bandwidth for scalable performance," May-June, 2006, pp. 41-57.
- Briz, J.L., see Alastruey, J., July-Aug., 2006, pp. 72-82.
- Brooks, D., see Wu, Q., Jan.-Feb., 2006, pp. 119-129.

C

- Calder, B., see Narayanasamy, S., Jan.-Feb., 2006, pp. 100-109.
- Calder, B., see Van Biesbrouck, M., July-Aug., 2006, pp. 32-42.
- Cantin, J.F., J.E. Smith, M.H. Lipasti, A. Moshovos, and B. Falsafi. "Coarse-grain coherence tracking: Region-Scout and Region Coherence Arrays," Jan.-Feb., 2006, pp. 70-79.
- Carter, J.B., see Balasubramonian, R., Nov.-Dec., 2006, pp. 40-52.
- Ceze, L., see Renau, J., Jan.-Feb., 2006, pp. 80-91.
- Cheng Liqun, see Balasubramonian, R., Nov.-Dec., 2006, pp. 40-52.
- Clark, D.W., see Wu, Q., Jan.-Feb., 2006, pp. 119-129.
- Connors, D., see Wu, Q., Jan.-Feb., 2006, pp. 119-129.

D

- De, V., see Unsal, O.S., Nov.-Dec., 2006, pp. 30-39.
- Dill, P., see Minkenberg, C., May-June, 2006, pp. 58-71.
- Dreslinski, R.G., see Binkert, N.L., July-Aug., 2006, pp. 52-60.
- Duato, J., see Garcia, P.J., Sept.-Oct., 2006, pp. 52-66.

E

- Eeckhout, L., see Van Biesbrouck, M., July-Aug., 2006, pp. 32-42.
- Emma, P.G., "The best patents of all" [Micro Innovations], March-April, 2006, pp. 84, 83.
- Emma, P.G., "Patent claims revisited: Examiners and trolls" [Micro Innovations], May-June, 2006, pp. 96, 94-95.
- Emma, P.G. "The mechanics of filing a patent" [Micro

Innovations], July-Aug., 2006, pp. 87-88.

Emma, P.G., "Prosecuting your patent" [Micro Innovations], Sept.-Oct., 2006, pp. 88, 87.

Emma, P.G., "Five strategies for overcoming obviousness" [Micro Innovations], Nov.-Dec., 2006, pp. 72, 70-71.

Ergin, O., see Unsal, O.S., Nov.-Dec., 2006, pp. 30-39.

F

Falsafi, B., see Cantin, J.F., Jan.-Feb., 2006, pp. 70-79

Falsafi, B., see Wenisch, T.F., July-Aug., 2006, pp. 18-31.

Feng Wu-chun, see Balaji, P., May-June, 2006, pp. 24-40.

Ferdman, M., see Wenisch, T.F., July-Aug., 2006, pp. 18-31.

Flachs, B., see Gschwind, M., March-April, 2006, pp. 10-24.

Flich, J., see Garcia, P.J., Sept.-Oct., 2006, pp. 52-66.

G

Gandhi, A., H. Akkary, R. Rajwar, S.T. Srinivasan, and K. Lai. "Scalable load and store processing in latency-tolerant processors," Jan.-Feb., 2006, pp. 30-39.

Gara, A., see Salapura, V., Sept.-Oct., 2006, pp. 67-81.

Garcia, P.J., F.J. Quiles, J. Flich, J. Duato, I. Johnson, and F. Naven. "Efficient, scalable congestion management for interconnection networks," Sept.-Oct., 2006, pp. 52-66.

Gomaa, M.A., and T.N. Vijaykumar. "Opportunistic transient-fault detection," Jan.-Feb., 2006, pp. 92-99.

Gonzalez, A., see Unsal, O.S., Nov.-Dec., 2006, pp. 30-39.

Gonzalez, R.E. "A software-configurable processor architecture," Sept.-Oct., 2006, pp. 42-51.

Gracia Perez, D., H. Berry, and O. Temam. "A sampling method focusing on practicality," Nov.-Dec., 2006, pp. 14-28.

Greenstein, S. "Format wars all over again" [Micro Economics], Jan.-Feb., 2006, pp. 7, 140.

Greenstein, S. "Room for a thousand flowers to bloom" [Micro Economics], May-June, 2006, pp. 6, 93.

Greenstein, S. "Andy's acceleration and Moore's momentum" [Micro Economics], March-April, 2006, pp. 7, 81-82.

Greenstein, S. "Legislating entrepreneurship: An oxymoron?" [Micro Economics], July-Aug., 2006, pp. 4, 86.

Greenstein, S. "Ubiquitous clicks and complements" [Micro Economics], Sept.-Oct., 2006, pp. 7-8.

Greenstein, S. "Four nightmares for net neutrality" [Micro Economics], Nov.-Dec., 2006, pp. 12-13.

Grzybowski, R., see Minkenberg, C., May-June, 2006, pp. 58-71.

Gschwind, M., H.P. Hofstee, B. Flachs, M. Hopkin, Y. Watanabe, and T. Yamazaki. "Synergistic processing in Cell's multicore architecture," March-April, 2006, pp. 10-24.

Gunn, C. "CMOS photonics for high-speed interconnects," March-April, 2006, pp. 58-66.

Gurumurthi, S., Youngjae Kim, and A. Sivasubramaniam.

"Using STEAM for thermal simulation of storage systems," July-Aug., 2006, pp. 43-51.

Gusat, M., see Minkenberg, C., May-June, 2006, pp. 58-71.

Gyunggho Lee, see Yong-Joon Park, July-Aug., 2006, pp. 62-71.

H

Hemenway, R.R., see Minkenberg, C., May-June, 2006, pp. 58-71.

Hoe, J.C., see Wenisch, T.F., July-Aug., 2006, pp. 18-31.

Hofstee, H.P., see Gschwind, M., March-April, 2006, pp. 10-24.

Hopkin, M., see Gschwind, M., March-April, 2006, pp. 10-24.

Hsu, L.R., see Binkert, N.L., July-Aug., 2006, pp. 52-60.

Hudson, T., see Brightwell, R., May-June, 2006, pp. 41-57.

Hur, I., and C. Lin. "Adaptive history-based memory schedulers for modern processors," Jan.-Feb., 2006, pp. 22-29.

Hwu, W.-m.W., see Barnes, R.D., Jan.-Feb., 2006, pp. 40-47.

Hyesoon Kim, see Mutlu, O., Jan.-Feb., 2006, pp. 10-20.

Hyesoon Kim, O. Mutlu, J. Stark, and Y.N. Patt. "Wish branches: Enabling adaptive and aggressive predicated execution," Jan.-Feb., 2006, pp. 48-58.

I

Ibanez, P., see Alastruey, J., July-Aug., 2006, pp. 72-82.

Iliadis, I., see Minkenberg, C., May-June, 2006, pp. 58-71.

Iyer, R., see Li Zhao, May-June, 2006, pp. 72-84.

J

Jha, N.K., see Shang, L., Jan.-Feb., 2006, pp. 130-139.

Johnson, I., see Garcia, P.J., Sept.-Oct., 2006, pp. 52-66.

K

Kastensmidt, F.L., see Neto, E.H., Sept.-Oct., 2006, pp. 10-18.

Kim, C.H., see Agarwal, A., March-April, 2006, pp. 68-80.

Kim Hyesoon, see Mutlu, O., Jan.-Feb., 2006, pp. 10-20.

Kim Hyesoon, see Hyesoon Kim, Jan.-Feb., 2006, pp. 48-58.

Kim Youngjae, see Gurumurthi, S., July-Aug., 2006, pp. 43-51.

Kistler, M., M. Perrone, and F. Petrini. "Cell multi-processor communication network: Built for speed," May-June, 2006, pp. 10-23.

Krishnamurthy, R., see Minkenberg, C., May-June, 2006, pp. 58-71.

Kumar Amit, see Shang, L., Jan.-Feb., 2006, pp. 130-139.

Kuszmaul, B.C., see Ananian, C.S., Jan.-Feb., 2006, pp.

59-69.

L

- Lai, K.**, see Gandhi, A., Jan.-Feb., 2006, pp. 30-39.
- Lee, J.**, see Wu, Q., Jan.-Feb., 2006, pp. 119-129.
- Lee Gyungho**, see Yong-Joon Park, July-Aug., 2006, pp. 62-71.
- Leiserson, C.E.**, see Ananian, C.S., Jan.-Feb., 2006, pp. 59-69.
- Lie, S.**, see Ananian, C.S., Jan.-Feb., 2006, pp. 59-69.
- Lim, K.T.**, see Binkert, N.L., July-Aug., 2006, pp. 52-60.
- Lin, C.**, see Hur, I., Jan.-Feb., 2006, pp. 22-29.
- Lindenstruth, V.** "An extreme processor for an extreme experiment," March-April, 2006, pp. 48-57.
- Lipasti, M.H.**, see Cantin, J.F., Jan.-Feb., 2006, pp. 70-79.
- Liquan Cheng**, see Balasubramonian, R., Nov.-Dec., 2006, pp. 40-52.
- Liu, W.**, see Renau, J., Jan.-Feb., 2006, pp. 80-91.
- Li Zhao**, Yan Luo, L.N. Bhuyan, and R. Iyer. "A network processor-based, content-aware switch," May-June, 2006, pp. 72-84.
- Luijten, R.**, see Minkenberg, C., May-June, 2006, pp. 58-71.
- Luo Yan**, see Li Zhao, May-June, 2006, pp. 72-84.
- Lysne, O.**, see Petrini, F., May-June, 2006, pp. 7-9.

M

- Martonosi, M.**, see Wu, Q., Jan.-Feb., 2006, pp. 119-129.
- Mateosian, R.** *The Singularity is Near: When Humans Transcend Biology* (Kurzweil, R.; 2005) [book review], Jan.-Feb., 2006, pp. 141-142.
- Mateosian, R.** *My Job Went to India: 52 Ways to Save Your Job* (Fowler, C.; 2005) [book review], Jan.-Feb., 2006, p. 142.
- Mateosian, R.** *Globalization and Its Enemies* (Cohen, D.; 2006) [book review], May-June, 2006, p. 86.
- Mateosian, R.** *In Defense of Globalization* (Bhagwati, J.; 2005) [book review], May-June, 2006, p. 86.
- Mateosian, R.** *The Art of SQL* (Faroult, S. and Robson, P.; 2006) [book review], May-June, 2006, pp. 86-87.
- Mateosian, R.** *About Face 2.0: The Essentials of Interaction Design* (Cooper, A. and Riemann, R.; 2003) [book review], May-June, 2006, p. 87.
- Mateosian, R.** *Deep Time of the Media: Toward an Archaeology of Hearing and Seeing Technical Means* (Zielinski, S.; 2006) [book review], May-June, 2006, p. 87.
- Mateosian, R.** *Weinberg on Writing - The Fieldstone Method* (Weinberg, G.M.; 2006) [book review], July-Aug., 2006, pp. 83-84.
- Mateosian, R.** *From Java to Ruby - Things Every Manager Should Know* (Tate, B.; 2006) [book review], July-Aug., 2006, pp. 84-85.
- Mateosian, R.** *The Art of Computer Programming, Vol. 4,*

Fascicle 4 (Knuth, D.;2006) [book review], July-Aug., 2006, p. 84.

- Mateosian, R.** *Introduction to DITA - A User Guide to the Darwin Information Typing Architecture* (Linton, J. and Bruski, K.) [book review], July-Aug., 2006, p. 85.
- Mateosian, R.** *Ajax Design Patterns - Creating Web 2.0 Sites with Programming and Usability Patterns* (Mahemoff, M.; 2006) [book review], Sept.-Oct., 2006, p. 82.
- Mateosian, R.** *Documenting APIs - Writing Developer Documentation for Java APIs and SDKs* (Bisso, J.F. and Bitzone, V.M.; 2006) [book review], Sept.-Oct., 2006, pp. 82-83.
- Mateosian, R.** *Interface Oriented Design* (Pugh, K.; 2006) [book review], Sept.-Oct., 2006, p. 82.
- Mateosian, R.** *.NET Internationalization - The Developer's Guide to Building Global Windows and Web Applications* (Smith-Ferrier, G.; 2007) [book review], Sept.-Oct., 2006, p. 83.
- Mateosian, R.** *CSS - The Missing Manual* (McFarland, S.; 2006) [book review], Sept.-Oct., 2006, p. 83.
- Mateosian, R.** *Communication Design - Developing Web Site Documentation for Design and Planning* (Brown, D.M.; 2006) [book review], Sept.-Oct., 2006, p. 83.
- Mateosian, R.** *Secrets of RSS* (Holzner, S.; 2006) [book review], Sept.-Oct., 2006, p. 83.
- Minkenberg, C.**, F. Abel, P. Muller, R. Krishnamurthy, M. Gusat, P. Dill, I. Iliadis, R. Luijten, R.R. Hemenway, R. Grzybowski, and E. Schiattarella. "Designing a crossbar scheduler for HPC applications," May-June, 2006, pp. 58-71.
- Moshovos, A.**, see Cantin, J.F., Jan.-Feb., 2006, pp. 70-79
- Mukhopadhyay, S.**, see Agarwal, A., March-April, 2006, pp. 68-80.
- Muller, P.**, see Minkenberg, C., May-June, 2006, pp. 58-71.
- Mundy, J.**, see Nepal, K., Sept.-Oct., 2006, pp. 19-27.
- Muralimanohar, N.**, see Balasubramonian, R., Nov.-Dec., 2006, pp. 40-52.
- Murmann, B.** "Digitally assisted analog circuits," March-April, 2006, pp. 38-47.
- Mutlu, O.**, Hyesoon Kim, and Y.N. Patt. "Efficient run-ahead execution: Power-efficient memory latency tolerance," Jan.-Feb., 2006, pp. 10-20.
- Mutlu, O.**, see Hyesoon Kim, Jan.-Feb., 2006, pp. 48-58.

N

- Nakano, J.**, see Teodorescu, R., Sept.-Oct., 2006, pp. 28-40.
- Narayanasamy, S.**, G. Pokam, and B. Calder. "BugNet: Recording application-level execution for deterministic replay debugging," Jan.-Feb., 2006, pp. 100-109.
- Naven, F.**, see Garcia, P.J., Sept.-Oct., 2006, pp. 52-66.
- Nepal, K.**, R.I. Bahar, J. Mundy, W.R. Patterson, and A.

Zaslavsky. "MRF reinforcer: A probabilistic element for space redundancy in nanoscale circuits," Sept.-Oct., 2006, pp. 19-27.

Neto, E.H., I. Ribeiro, M. Vieira, G. Wirth, and F.L. Kastensmidt. "Using bulk built-in current sensors to detect soft errors," Sept.-Oct., 2006, pp. 10-18.

P

Panda, D.K., see Balaji, P., May-June, 2006, pp. 24-40.

Park Yong-Joon, see Yong-Joon Park, July-Aug., 2006, pp. 62-71.

Patt, Y.N., see Mutlu, O., Jan.-Feb., 2006, pp. 10-20.

Patt, Y.N., see Hyesoon Kim, Jan.-Feb., 2006, pp. 48-58.

Patterson, W.R., see Nepal, K., Sept.-Oct., 2006, pp. 19-27.

Peh, L.-S., see Shang, L., Jan.-Feb., 2006, pp. 130-139.

Perrone, M., see Kistler, M., May-June, 2006, pp. 10-23.

Petrini, F., O. Lysne, and R. Brightwell. "Guest editors' introduction: High-performance interconnects [special section intro.]," May-June, 2006, pp. 7-9.

Petrini, F., see Kistler, M., May-June, 2006, pp. 10-23.

Pokam, G., see Narayanasamy, S., Jan.-Feb., 2006, pp. 100-109.

Predretti, K.T., see Brightwell, R., May-June, 2006, pp. 41-57.

Q

Quiles, F.J., see Garcia, P.J., Sept.-Oct., 2006, pp. 52-66.

R

Rajwar, R., see Gandhi, A., Jan.-Feb., 2006, pp. 30-39.

Ramani, K., see Balasubramonian, R., Nov.-Dec., 2006, pp. 40-52.

Raychowdhury, A., see Agarwal, A., March-April, 2006, pp. 68-80.

Reddi, V.J., see Wu, Q., Jan.-Feb., 2006, pp. 119-129.

Reinhardt, S.K., see Binkert, N.L., July-Aug., 2006, pp. 52-60.

Renau, J., K. Strauss, L. Ceze, W. Liu, S.R. Sarangi, J. Tuck, and J. Torrellas. "Energy-efficient thread-level speculation," Jan.-Feb., 2006, pp. 80-91.

Ribeiro, I., see Neto, E.H., Sept.-Oct., 2006, pp. 10-18.

Roy, K., see Agarwal, A., March-April, 2006, pp. 68-80.

Ryoo, S., see Barnes, R.D., Jan.-Feb., 2006, pp. 40-47.

S

Saidi, A.G., see Binkert, N.L., July-Aug., 2006, pp. 52-60.

Salapura, V., R. Walkup, and A. Gara. "Exploiting workload parallelism for performance and power of optimization in Blue Gene," Sept.-Oct., 2006, pp. 67-81.

Sarangi, S.R., see Renau, J., Jan.-Feb., 2006, pp. 80-91.

Schiattarella, E., see Minkenbergh, C., May-June, 2006, pp. 58-71.

Sell, J., and A.J. Smith. "Guest editors' introduction: Hot Chips 17," March-April, 2006, pp. 8-9.

Shang, L., L.-S. Peh, Amit Kumar, and N.K. Jha. "Temperature-aware on-chip networks," Jan.-Feb., 2006, pp. 130-139.

Sherwood, T., see Tan, L., Jan.-Feb., 2006, pp. 110-117.

Sherwood, T., and J.J. Yi. "Guest editors' introduction: Computer architecture simulation and modeling [special section intro.]," July-Aug., 2006, pp. 5-7.

Sivasubramaniam, A., see Gurumurthi, S., July-Aug., 2006, pp. 43-51.

Smith, A.J., see Sell, J., March-April, 2006, pp. 8-9.

Smith, J.E., see Cantin, J.F., Jan.-Feb., 2006, pp. 70-79.

Srinivasan, S.T., see Gandhi, A., Jan.-Feb., 2006, pp. 30-39.

Stark, J., see Hyesoon Kim, Jan.-Feb., 2006, pp. 48-58.

Stern, R. "Court dismisses 'copyright champion's' source code copyright suit," May-June, 2006, pp. 88-90.

Stern, R. "New Jersey federal court holds Qualcomm's unFRANDly acts no antitrust violation [Micro Law]," Sept.-Oct., 2006, pp. 9, 84-86.

Strauss, K., see Renau, J., Jan.-Feb., 2006, pp. 80-91.

T

Tan, L., and T. Sherwood. "Architectures for bit-split string scanning in intrusion detection," Jan.-Feb., 2006, pp. 110-117.

Temam, O., see Gracia Perez, D., Nov.-Dec., 2006, pp. 14-28.

Teodorescu, R., J. Nakano, and J. Torrellas. "Swich: A prototype for efficient cache-level checkpointing and rollback," Sept.-Oct., 2006, pp. 28-40.

Torrellas, J. "Guest editor's introduction: Micro's top picks from microarchitecture conferences [special section intro.]," Jan.-Feb., 2006, pp. 8-9.

Torrellas, J., see Renau, J., Jan.-Feb., 2006, pp. 80-91.

Torrellas, J., see Teodorescu, R., Sept.-Oct., 2006, pp. 28-40.

Tschanz, J.W., see Unsal, O.S., Nov.-Dec., 2006, pp. 30-39.

Tuck, J., see Renau, J., Jan.-Feb., 2006, pp. 80-91.

U

Underwood, K.D., see Brightwell, R., May-June, 2006, pp. 41-57.

Unsal, O.S., J.W. Tschanz, K. Bowman, V. De, X. Vera, A. Gonzalez, and O. Ergin. "Impact of parameter variations on circuits and microarchitecture," Nov.-Dec., 2006, pp. 30-39.

V

Van Biesbrouck, M., B. Calder, and L. Eeckhout. "Efficient sampling startup for SimPoint," July-Aug., 2006,

pp. 32-42.

Vera, X., see Unsal, O.S., Nov.-Dec., 2006, pp. 30-39.

Vieira, M., see Neto, E.H., Sept.-Oct., 2006, pp. 10-18.

Vijaykumar, T.N., see Gomas, M.A., Jan.-Feb., 2006, pp. 92-99.

Vinals, V., see Alastruey, J., July-Aug., 2006, pp. 72-82.

W

Walkup, R., see Salapura, V., Sept.-Oct., 2006, pp. 67-81.

Watanabe, Y., see Gschwind, M., March-April, 2006, pp. 10-24.

Wenisch, T.F., R.E. Wunderlich, M. Ferdman, A. Ailamaki, B. Falsafi, and J.C. Hoe. "SimFlex: Statistical sampling of computer system simulation," July-Aug., 2006, pp. 18-31.

Wirth, G., see Neto, E.H., Sept.-Oct., 2006, pp. 10-18.

Wood, D.A., see Alameldeen, A.R., July-Aug., 2006, pp. 8-17.

Wu, Q., M. Martonosi, D.W. Clark, V.J. Reddi, D. Connors, Y. Wu, J. Lee, and D. Brooks. "Dynamic-compiler-driven control for microprocessor energy and performance," Jan.-Feb., 2006, pp. 119-129.

Wu, Y., see Wu, Q., Jan.-Feb., 2006, pp. 119-129.

Wu-chun Feng, see Balaji, P., May-June, 2006, pp. 24-40.

Wunderlich, R.E., see Wenisch, T.F., July-Aug., 2006, pp. 18-31.

Y

Yamazaki, T., see Gschwind, M., March-April, 2006, pp. 10-24.

Yan Luo, see Li Zhao, May-June, 2006, pp. 72-84.

Yi, J.J., see Sherwood, T., July-Aug., 2006, pp. 5-7.

Yong-Joon Park, Zhao Zhang, and Gyungho Lee. "Microarchitectural protection against stack-based buffer overflow attacks," July-Aug., 2006, pp. 62-71.

Youngjae Kim, see Gurumurthi, S., July-Aug., 2006, pp. 43-51.

Z

Zaslavsky, A., see Nepal, K., Sept.-Oct., 2006, pp. 19-27.

Zhang Zhao, see Yong-Joon Park, July-Aug., 2006, pp. 62-71.

Zhao Li, see Li Zhao, May-June, 2006, pp. 72-84.

Zhao Zhang, see Yong-Joon Park, July-Aug., 2006, pp. 62-71.

SUBJECT INDEX

A

Accelerators

extreme expt., extreme processor. Lindenstruth, V., March-April, 2006, pp. 48-57.

Analog circuits

digitally assisted analog ccts.. Murmann, B., March-April,

2006, pp. 38-47.

Analog-digital conversion

digitally assisted analog ccts.. Murmann, B., March-April, 2006, pp. 38-47.

Application programming interfaces

book review; *Documenting APIs - Writing Developer Documentation for JavaSPIs and SDKs* (Bisso, J.F. and Bitzone, V.M.; 2006). Mateosian, R., Sept.-Oct., 2006, pp. 82-83.

Application specific integrated circuits

scalable perform., seastar interconnect, balanced bandwidth. Brightwell, R., et al., May-June, 2006, pp. 41-57.

Application specific integrated circuits; cf. Mixed analog-digital integrated circuits

B

Bandwidth management

efficient, scalable congestion management for interconnection networks. Garcia, P.J., et al., Sept.-Oct., 2006, pp. 52-66.

Book reviews

About Face 2.0: The Essentials of Interaction Design (Cooper, A. and Riemann, R.; 2003). Mateosian, R., May-June, 2006, p. 87.

Ajax Design Patterns - Creating Web 2.0 Sites with Programming and Usability Patterns (Mahemoff, M.; 2006). Mateosian, R., Sept.-Oct., 2006, p. 82.

Communication Design - Developing Web Site Documentation for Design and Planning (Brown, D.M.; 2006). Mateosian, R., Sept.-Oct., 2006, p. 83.

CSS - The Missing Manual (McFarland, S.; 2006). Mateosian, R., Sept.-Oct., 2006, p. 83.

Deep Time of the Media: Toward an Archaeology of Hearing and Seeing Technical Means (Zielinski, S.; 2006). Mateosian, R., May-June, 2006, p. 87.

Documenting APIs - Writing Developer Documentation for JavaSPIs and SDKs (Bisso, J.F. and Bitzone, V.M.; 2006). Mateosian, R., Sept.-Oct., 2006, pp. 82-83.

From Java to Ruby - Things Every Manager Should Know (Tate, B.; 2006). Mateosian, R., July-Aug., 2006, pp. 84-85.

Globalization and Its Enemies (Cohen, D.; 2006). Mateosian, R., May-June, 2006, p. 86.

In Defense of Globalization (Bhagwati, J.; 2005). Mateosian, R., May-June, 2006, p. 86.

Interface Oriented Design (Pugh, K.; 2006). Mateosian, R., Sept.-Oct., 2006, p. 82.

Introduction to DITA - A User Guide to the Darwin Information Typing Architecture (Linton, J. and Bruski, K.). Mateosian, R., July-Aug., 2006, p. 85.

My Job Went to India: 52 Ways to Save Your Job (Fowler, C.; 2005). Mateosian, R., Jan.-Feb., 2006, p. 142.

.NET Internationalization - The Developer's Guide to Building

Global Windows and Web Applications (Smith-Ferrier, G.; 2007). Mateosian, R., Sept.-Oct., 2006, p. 83.

Secrets of RSS (Holzner, S.; 2006). Mateosian, R., Sept.-Oct., 2006, p. 83.

The Art of Computer Programming, Vol. 4, Fascicle 4 (Knuth, D.; 2006). Mateosian, R., July-Aug., 2006, p. 84.

The Art of SQL (Faroult, S. and Robson, P.; 2006). Mateosian, R., May-June, 2006, pp. 86-87.

The Singularity is Near: When Humans Transcend Biology (Kurzweil, R.; 2005). Mateosian, R., Jan.-Feb., 2006, pp. 141-142.

Weinberg on Writing - The Fieldstone Method (Weinberg, G.M.; 2006). Mateosian, R., July-Aug., 2006, pp. 83-84.

Broadband networks

four nightmares for net neutrality. Greenstein, S., Nov.-Dec., 2006, pp. 12-13.

Buffer memories

microarchitectural protection against stack-based buffer overflow attacks. Yong-Joon Park, et al., July-Aug., 2006, pp. 62-71.

Buffer storage; cf. Cache storage

Business communication

book review; *Weinberg on Writing - The Fieldstone Method* (Weinberg, G.M.; 2006). Mateosian, R., July-Aug., 2006, pp. 83-84.

C

Cache memories

coarse-grain coherence tracking, RegionScout and Region Coherence Arrays. Cantin, J.F., et al., Jan.-Feb., 2006, pp. 70-79.

efficient run-ahead execution, power-efficient memory latency tolerance. Mutlu, O., et al., Jan.-Feb., 2006, pp. 10-20.

latency-tolerant processors, scalable load and store proc.. Gandhi, A., et al., Jan.-Feb., 2006, pp. 30-39.

software demand, hardware supply. Alastruey, J., et al., July-Aug., 2006, pp. 72-82.

tolerating cache-miss latency, multipass pipelines. Barnes, R.D., et al., Jan.-Feb., 2006, pp. 40-47.

Cache storage

Swich, prototype for efficient cache-level checkpointing and rollback. Teodorescu, R., et al., Sept.-Oct., 2006, pp. 28-40.

Circuit theory; cf. Network parameters

Client-server systems

net. processor-based, content-aware switch. Li Zhao, et al., May-June, 2006, pp. 72-84.

CMOSFET logic devices

nanoscale ccts., leakage power anal. and reduction. Agarwal, A., et al., March-April, 2006, pp. 68-80.

CMOS integrated circuits

high-speed interconnects, CMOS photonics. Gunn, C., March-April, 2006, pp. 58-66.

MRF reinforcer, a probabilistic element for space redundancy in nanoscale circuits. Nepal, K., et al., Sept.-Oct., 2006, pp. 19-27.

Communication system routing

net. processor-based, content-aware switch. Li Zhao, et al., May-June, 2006, pp. 72-84.

Communication system software

M5 simulator, modeling networked systs.. Binkert, N.L., et al., July-Aug., 2006, pp. 52-60.

computer architecture

computer architecture simulation and modeling (special section). July-Aug., 2006, pp. 5-82.

computer architecture simulation and modeling (special section intro.). Sherwood, T., et al., July-Aug., 2006, pp. 5-7.

Micro's top picks from microarchitecture conferences (special section). Jan.-Feb., 2006, pp. 8-139.

Micro's top picks from microarchitecture conferences (special section intro.). Torrellas, J., Jan.-Feb., 2006, pp. 8-9.

microarchitectural protection against stack-based buffer overflow attacks. Yong-Joon Park, et al., July-Aug., 2006, pp. 62-71.

net. processor-based, content-aware switch. Li Zhao, et al., May-June, 2006, pp. 72-84.

software-configurable processor architecture. Gonzalez, R.E., Sept.-Oct., 2006, pp. 42-51.

speed, cell multiprocessor commun. net., built. Kistler, M., et al., May-June, 2006, pp. 10-23.

Xbox 360 syst. archit.. Andrews, J., et al., March-April, 2006, pp. 25-37.

Computer architecture; cf. Memory architecture; Parallel architectures; Reduced instruction set computing

Computer fault tolerance

tolerating cache-miss latency, multipass pipelines. Barnes, R.D., et al., Jan.-Feb., 2006, pp. 40-47.

Computer instructions

efficient run-ahead execution, power-efficient memory latency tolerance. Mutlu, O., et al., Jan.-Feb., 2006, pp. 10-20.

latency-tolerant processors, scalable load and store proc.. Gandhi, A., et al., Jan.-Feb., 2006, pp. 30-39.

multiprocessor workloads, IPC considered harmful. Alameldeen, A.R., et al., July-Aug., 2006, pp. 8-17.

software demand, hardware supply. Alastruey, J., et al., July-Aug., 2006, pp. 72-82.

tolerating cache-miss latency, multipass pipelines. Barnes, R.D., et al., Jan.-Feb., 2006, pp. 40-47.

unbounded transaction memory. Ananian, C.S., et al., Jan.-Feb., 2006, pp. 59-69.

Computer interfaces; cf. Network interfaces; Peripheral interfaces

Computer networks; cf. Local area networks

Computer Software

book review; *Introduction to DITA - A User Guide to the Darwin Information Typing Architecture* (Linton, J. and Bruski, K.). Mateosian, R., July-Aug., 2006, p. 85.

software-configurable processor architecture. Gonzalez, R.E., Sept.-Oct., 2006, pp. 42-51.

Consumer electronics

recording media formats. Greenstein, S., Jan.-Feb., 2006, pp. 7, 140.

Content management

book review; *Secrets of RSS* (Holzner, S.; 2006). Mateosian, R., Sept.-Oct., 2006, p. 83.

Copyright protection

court dismisses copyright champion's source code copyright suit. Stern, R., May-June, 2006, pp. 88-90.

filing, patent, mech.. Emma, P.G., July-Aug., 2006, pp. 87-88.

D

Database management systems

exploiting workload parallelism for performance and power of optimization in Blue Gene. Salapura, V., et al., Sept.-Oct., 2006, pp. 67-81.

Data buses

bridging Ethernet-Ethernot perform. gap. Balaji, P., et al., May-June, 2006, pp. 24-40.

Data conversion; cf. Analog-digital conversion; Digital-analog conversion

Data handling; cf. Electronic data interchange; String matching

Data security

bit-split string scanning, intrusion detect., archits.. Tan, L., et al., Jan.-Feb., 2006, pp. 110-117.

Data structures

latency-tolerant processors, scalable load and store proc.. Gandhi, A., et al., Jan.-Feb., 2006, pp. 30-39.

modern processors, adaptive hist.-based memory schedulers. Hur, I., et al., Jan.-Feb., 2006, pp. 22-29.

Design

book review; *About Face 2.0: The Essentials of Interaction Design* (Cooper, A. and Riemann, R.; 2003). Mateosian, R., May-June, 2006, p. 87.

book review; *Interface Oriented Design* (Pugh, K.; 2006). Mateosian, R., Sept.-Oct., 2006, p. 82.

book review; *The Art of SQL* (Faroult, S. and Robson, P.; 2006). Mateosian, R., May-June, 2006, pp. 86-87.

Design engineering; cf. Product development

Digital-analog conversion

digitally assisted analog ccts.. Murmann, B., March-April, 2006, pp. 38-47.

Digital circuits; cf. Digital filters

Digital computers; cf. Mainframes; Parallel machines

Digital filters

extreme expt., extreme processor. Lindenstruth, V., March-April, 2006, pp. 48-57.

Digital integrated circuits; cf. Microprocessor chips

Digital simulation; cf. Discrete event simulation

Discrete event simulation

M5 simulator, modeling networked systs.. Binkert, N.L., et al., July-Aug., 2006, pp. 52-60.

Distributed memory systems

scalable perform., seastar interconnect, balanced bandwidth. Brightwell, R., et al., May-June, 2006, pp. 41-57.

Distributed processing; cf. Client-server systems; Pipeline processing; Processor scheduling

DRAM chips

modern processors, adaptive hist.-based memory schedulers. Hur, I., et al., Jan.-Feb., 2006, pp. 22-29.

New Jersey federal court holds Qualcomm's unFRANDly acts no antitrust violation. Stern, R., Sept.-Oct., 2006, pp. 9, 84-86.

E

Economics

four nightmares for net neutrality. Greenstein, S., Nov.-Dec., 2006, pp. 12-13.

Electron device manufacture; cf. Integrated circuit manufacture

Electronic data interchange

recording media formats. Greenstein, S., Jan.-Feb., 2006, pp. 7, 140.

Electronic publishing; cf. Content management

Electronics industry; cf. Consumer electronics

Energy management systems; cf. Load management

Error detection

using bulk built-in current sensors to detect soft errors. Neto, E.H., et al., Sept.-Oct., 2006, pp. 10-18.

F

Fault currents; cf. Leakage currents

Fault diagnosis

opportunistic transient-fault detect.. Goma, M.A., et al., Jan.-Feb., 2006, pp. 92-99.

File organization; cf. Database management systems; Data structures

Filters; cf. Digital filters

G

Games

Xbox 360 syst. archit.. Andrews, J., et al., March-April, 2006, pp. 25-37.

Globalization

book review; *Globalization and Its Enemies* (Cohen, D.; 2006). Mateosian, R., May-June, 2006, p. 86.

book review; *In Defense of Globalization* (Bhagwati, J.; 2005).
Mateosian, R., May-June, 2006, p. 86.

H

High-speed techniques

Hot Interconnects 12 (special section). March-April, 2006,
pp. 8-80.

Hot Interconnects 12 (special section intro.). Sell, J., et al.,
March-April, 2006, pp. 8-9.

Human factors

book review; *The Singularity is Near: When Humans Transcend Biology* (Kurzweil, R.; 2005). Mateosian, R., Jan.-
Feb., 2006, pp. 141-142.

I

Industrial property; cf. Patents

Information networks; cf. Internet

Information resources; cf. Web sites

Information retrieval systems; cf. Online front-ends

Information systems; cf. Database management systems

Instruments; cf. Sensors

Integrated circuit design

andy's accel. and Moore's momentum. Greenstein, S.,
March-April, 2006, pp. 7, 81-82.

scalable perform., seastar interconnect, balanced bandwidth.
Brightwell, R., et al., May-June, 2006, pp. 41-57.

Integrated circuit interconnections

scalable perform., seastar interconnect, balanced bandwidth.
Brightwell, R., et al., May-June, 2006, pp. 41-57.

Integrated circuit manufacture

andy's accel. and Moore's momentum. Greenstein, S.,
March-April, 2006, pp. 7, 81-82.

Integrated circuits

using bulk built-in current sensors to detect soft errors. Neto,
E.H., et al., Sept.-Oct., 2006, pp. 10-18.

Integrated circuit technology; cf. Integrated circuit interconnections

Integrated memory circuits; cf. DRAM chips

Integrated optics

high-speed interconnects, CMOS photonics. Gunn, C.,
March-April, 2006, pp. 58-66.

Interconnected systems

Hot Interconnects 12 (special section). March-April, 2006,
pp. 8-80.

Hot Interconnects 12 (special section intro.). Sell, J., et al.,
March-April, 2006, pp. 8-9.

Interconnections

efficient, scalable congestion management for interconnection networks. Garcia, P.J., et al., Sept.-Oct., 2006,
pp. 52-66.

leveraging wire properties at the microarchitecture level. Balasubramonian, R., et al., Nov.-Dec., 2006, pp. 40-52.

Interconnections; cf. Integrated circuit interconnections;

Optical interconnections

Internet

book review; *Deep Time of the Media: Toward an Archaeology of Hearing and Seeing Technical Means* (Zielinski, S.; 2006). Mateosian, R., May-June, 2006, p. 87.

net. processor-based, content-aware switch. Li Zhao, et al.,
May-June, 2006, pp. 72-84.

J

Java

book review; *Ajax Design Patterns - Creating Web 2.0 Sites with Programming and Usability Patterns* (Mahemoff, M.; 2006). Mateosian, R., Sept.-Oct., 2006, p. 82.

book review; *Documenting APIs - Writing Developer Documentation for JavaSPIs and SDKs* (Bisso, J.F. and Bitzone, V.M.; 2006). Mateosian, R., Sept.-Oct., 2006,
pp. 82-83.

book review; *From Java to Ruby - Things Every Manager Should Know* (Tate, B.; 2006). Mateosian, R., July-Aug.,
2006, pp. 84-85.

L

Large-scale systems; cf. Interconnected systems

Law

New Jersey federal court holds Qualcomm's unFRANDly acts no antitrust violation. Stern, R., Sept.-Oct., 2006,
pp. 9, 84-86.

Leakage currents

nanoscale ccts., leakage power anal. and reduction. Agarwal, A., et al., March-April, 2006, pp. 68-80.

Legal factors

court dismisses copyright champion's source code copyright suit. Stern, R., May-June, 2006, pp. 88-90.

filing, patent, mech.. Emma, P.G., July-Aug., 2006, pp. 87-88.
legislating entrepreneurship, oxymoron. Greenstein, S., July-Aug., 2006, pp. 4, 86.

patent claims, examiners and trolls. Emma, P.G., May-June, 2006, pp. 96, 94-95.

Load (electric); cf. Load management

Load management

exploiting workload parallelism for performance and power of optimization in Blue Gene. Salapura, V., et al., Sept.-Oct., 2006, pp. 67-81.

Local area networks

bridging Ethernet-Ethernot perform. gap. Balaji, P., et al.,
May-June, 2006, pp. 24-40.

Local area networks; cf. Optical fiber LAN

Logic; cf. Logic design

Logic design

Cell's multicore archit., synergistic proc.. Gschwind, M., et al., March-April, 2006, pp. 10-24.

speed, cell multiprocessor commun. net., built. Kistler, M., et al., May-June, 2006, pp. 10-23.

M

Mainframes

exploiting workload parallelism for performance and power of optimization in Blue Gene. Salapura, V., et al., Sept.-Oct., 2006, pp. 67-81.

Management

book review; *From Java to Ruby - Things Every Manager Should Know* (Tate, B.; 2006). Mateosian, R., July-Aug., 2006, pp. 84-85.

Management; cf. Content management; Outsourcing

Man-machine systems

book review; *The Singularity is Near: When Humans Transcend Biology* (Kurzweil, R.; 2005). Mateosian, R., Jan.-Feb., 2006, pp. 141-142.

Market opportunities

market value for nascent goods. Greenstein, S., May-June, 2006, pp. 6, 93.

Materials; cf. Nanostructured materials

Meetings

Hot Interconnects 12 (special section). March-April, 2006, pp. 8-80.

Hot Interconnects 12 (special section intro.). Sell, J., et al., March-April, 2006, pp. 8-9.

Micro's top picks from microarchitecture conferences (special section). Jan.-Feb., 2006, pp. 8-139.

Micro's top picks from microarchitecture conferences (special section intro.). Torrellas, J., Jan.-Feb., 2006, pp. 8-9.

Memory architecture

efficient run-ahead execution, power-efficient memory latency tolerance. Mutlu, O., et al., Jan.-Feb., 2006, pp. 10-20.

latency-tolerant processors, scalable load and store proc.. Gandhi, A., et al., Jan.-Feb., 2006, pp. 30-39.

SimPoint, efficient sampling startup. Van Biesbrouck, M., et al., July-Aug., 2006, pp. 32-42.

thermal simul. of storage systs., STEAM. Gurumurthi, S., et al., July-Aug., 2006, pp. 43-51.

unbounded transaction memory. Ananian, C.S., et al., Jan.-Feb., 2006, pp. 59-69.

Memory management

modern processors, adaptive hist.-based memory schedulers. Hur, I., et al., Jan.-Feb., 2006, pp. 22-29.

Microprocessor chips

leveraging wire properties at the microarchitecture level. Balasubramonian, R., et al., Nov.-Dec., 2006, pp. 40-52.

parameter variations on circuits and microarchitecture. Unsal, O.S., Nov.-Dec., 2006, pp. 30-39.

Microprocessors

Cell's multicore archit., synergistic proc.. Gschwind, M., et al., March-April, 2006, pp. 10-24.

energy and perform., dyn.-compiler-driven control. Wu, Q., et al., Jan.-Feb., 2006, pp. 119-129.

energy-efficient thread-level speculation. Renau, J., et al., Jan.-

Feb., 2006, pp. 80-91.

extreme expt., extreme processor. Lindenstruth, V., March-April, 2006, pp. 48-57.

net. processor-based, content-aware switch. Li Zhao, et al., May-June, 2006, pp. 72-84.

opportunistic transient-fault detect.. Goma, M.A., et al., Jan.-Feb., 2006, pp. 92-99.

scalable perform., seastar interconnect, balanced bandwidth. Brightwell, R., et al., May-June, 2006, pp. 41-57.

software demand, hardware supply. Alastruey, J., et al., July-Aug., 2006, pp. 72-82.

speed, cell multiprocessor commun. net., built. Kistler, M., et al., May-June, 2006, pp. 10-23.

Xbox 360 syst. archit.. Andrews, J., et al., March-April, 2006, pp. 25-37.

Mixed analog-digital integrated circuits

digitally assisted analog ccts.. Murmann, B., March-April, 2006, pp. 38-47.

Modeling

computer architecture simulation and modeling (special section). July-Aug., 2006, pp. 5-82.

computer architecture simulation and modeling (special section intro.). Sherwood, T., et al., July-Aug., 2006, pp. 5-7.

Monolithic integrated circuits; cf. Application specific integrated circuits

MOS integrated circuits; cf. CMOS integrated circuits

Multiprocessing

computer syst. simul., SimFlex, stat. sampling. Wenisch, T.F., et al., July-Aug., 2006, pp. 18-31.

energy-efficient thread-level speculation. Renau, J., et al., Jan.-Feb., 2006, pp. 80-91.

extreme expt., extreme processor. Lindenstruth, V., March-April, 2006, pp. 48-57.

latency-tolerant processors, scalable load and store proc.. Gandhi, A., et al., Jan.-Feb., 2006, pp. 30-39.

multiprocessor workloads, IPC considered harmful. Alameldeen, A.R., et al., July-Aug., 2006, pp. 8-17.

speed, cell multiprocessor commun. net., built. Kistler, M., et al., May-June, 2006, pp. 10-23.

Multiprocessing systems; cf. Distributed memory systems; Shared memory systems

Multiprocessor interconnection

temp.-aware on-chip nets.. Shang, L., et al., Jan.-Feb., 2006, pp. 130-139.

N

Nanostructured materials

MRF reinforcer, a probabilistic element for space redundancy in nanoscale circuits. Nepal, K., et al., Sept.-Oct., 2006, pp. 19-27.

Nanotechnology

nanoscale ccts., leakage power anal. and reduction. Agarwal,

A., et al., March-April, 2006, pp. 68-80.

Network interfaces

scalable perform., seastar interconnect, balanced bandwidth.

Brightwell, R., et al., May-June, 2006, pp. 41-57.

Network parameters

parameter variations on circuits and microarchitecture. Unsal,

O.S., Nov.-Dec., 2006, pp. 30-39.

Networks (circuits); cf. Analog circuits; Integrated circuits

Network servers

processor-based, content-aware switch. Li Zhao, et al., May-

June, 2006, pp. 72-84.

Network synthesis; cf. Integrated circuit design

I

Object-oriented languages; cf. Java

Object-oriented programming

book review; *Interface Oriented Design* (Pugh, K.; 2006).

Mateosian, R., Sept.-Oct., 2006, p. 82.

Online front-ends

book review; *CSS - The Missing Manual* (McFarland, S.;

2006). Mateosian, R., Sept.-Oct., 2006, p. 83.

Operations research; cf. Scheduling

Optical communication; cf. Optical fiber communication

Optical computing

HPC appls., designing, crossbar scheduler. Minkenberg, C.,

et al., May-June, 2006, pp. 58-71.

Optical fiber communication

HPC appls., designing, crossbar scheduler. Minkenberg, C.,

et al., May-June, 2006, pp. 58-71.

Optical fiber LAN

high-speed interconnects, CMOS photonics. Gunn, C.,

March-April, 2006, pp. 58-66.

Optical fiber networks; cf. Optical fiber LAN

Optical information processing; cf. Optical computing

Optical interconnections

high-speed interconnects, CMOS photonics. Gunn, C.,

March-April, 2006, pp. 58-66.

HPC appls., designing, crossbar scheduler. Minkenberg, C.,

et al., May-June, 2006, pp. 58-71.

Optical memories

HPC appls., designing, crossbar scheduler. Minkenberg, C.,

et al., May-June, 2006, pp. 58-71.

Optical switches

HPC appls., designing, crossbar scheduler. Minkenberg, C.,

et al., May-June, 2006, pp. 58-71.

Optics; cf. Integrated optics

Optimizing compilers

microprocessor energy and perform., dyn.-compiler-driven

control. Wu, Q., et al., Jan.-Feb., 2006, pp. 119-129.

Outsourcing

book review; *My Job Went to India: 52 Ways to Save Your*

Job (Fowler, C.; 2005). Mateosian, R., Jan.-Feb., 2006,

pp. 142.

P

Packet switching

net. processor-based, content-aware switch. Li Zhao, et al.,

May-June, 2006, pp. 72-84.

Parallel architectures

Cell's multicore archit., synergistic proc.. Gschwind, M., et

al., March-April, 2006, pp. 10-24.

extreme expt., extreme processor. Lindenstruth, V., March-

April, 2006, pp. 48-57.

Parallel machines

HPC appls., designing, crossbar scheduler. Minkenberg, C.,

et al., May-June, 2006, pp. 58-71.

Parallel processing; cf. Parallel architectures; Parallel machines

Patents

all, best patents. Emma, P.G., March-April, 2006, pp. 84, 83.

court dismisses copyright champion's source code copy-

right suit. Stern, R., May-June, 2006, pp. 88-90.

filing, patent, mech.. Emma, P.G., July-Aug., 2006, pp.

87-88.

how to write a patent. Jan.-Feb., 2006, pp. 8-139.

five strategies for overcoming obviousness. Emma, P.G.,

Nov.-Dec., 2006, pp. 72, 70-71.

patent claims, examiners and trolls. Emma, P.G., May-

June, 2006, pp. 96, 94-95.

prosecuting your patent. Emma, P.G., Sept.-Oct., 2006,

pp. 88, 87.

Pattern matching; cf. String matching

Peripheral interfaces

high-performance interconnects (special section). May-June,

2006, pp. 7-84.

high-performance interconnects (special section intro.). Petri-

ni, F., et al., May-June, 2006, pp. 7-9.

Pipeline processing

tolerating cache-miss latency, multipass pipelines. Barnes,

R.D., et al., Jan.-Feb., 2006, pp. 40-47.

Power demand

coarse-grain coherence tracking, RegionScout and Region

Coherence Arrays. Cantin, J.F., et al., Jan.-Feb., 2006,

pp. 70-79.

efficient run-ahead execution, power-efficient memory

latency tolerance. Mutlu, O., et al., Jan.-Feb., 2006,

pp. 10-20.

energy-efficient thread-level speculation. Renau, J., et al., Jan.-

Feb., 2006, pp. 80-91.

microprocessor energy and perform., dyn.-compiler-driven

control. Wu, Q., et al., Jan.-Feb., 2006, pp. 119-129.

opportunistic transient-fault detect.. Goma, M.A., et al.,

Jan.-Feb., 2006, pp. 92-99.

temp.-aware on-chip nets.. Shang, L., et al., Jan.-Feb., 2006,

pp. 130-139.

Processor scheduling

HPC appls., designing, crossbar scheduler. Minkenberg, C.,

et al., May-June, 2006, pp. 58-71.

Product development

andy's accel. and Moore's momentum. Greenstein, S., March-April, 2006, pp. 7, 81-82.

Program compilers

Cell's multicore archit., synergistic proc.. Gschwind, M., et al., March-April, 2006, pp. 10-24.

wish branches, enabling adaptive/aggressive predicated execution. Hyesoon Kim, et al., Jan.-Feb., 2006, pp. 48-58.

Program compilers; cf. Optimizing compilers

Program control structures

wish branches, enabling adaptive/aggressive predicated execution. Hyesoon Kim, et al., Jan.-Feb., 2006, pp. 48-58.

Programming

book review; *About Face 2.0: The Essentials of Interaction Design* (Cooper, A. and Riemann, R.; 2003). Mateosian, R., May-June, 2006, p. 7.

book review; *The Art of Computer Programming, Vol. 4, Fascicle 4* (Knuth, D.; 2006). Mateosian, R., July-Aug., 2006, p. 4.

Programming; cf. Object-oriented programming; Program control structures; Software tools; System documentation

Program processors; cf. Program compilers

Project engineering; cf. Scheduling

Protocols

bridging Ethernet-Ethernot perform. gap. Balaji, P., et al., May-June, 2006, pp. 24-40.

Protocols; cf. Transport protocols

Q

Query languages; cf. SQL

R

Radiation detectors

extreme expt., extreme processor. Lindenstruth, V., March-April, 2006, pp. 48-57.

Random-access storage; cf. DRAM chips

Recording

recording media formats. Greenstein, S., Jan.-Feb., 2006, pp. 7, 140.

Recovery

Swich, prototype for efficient cache-level checkpointing and rollback. Teodorescu, R., et al., Sept.-Oct., 2006, pp. 28-40.

Reduced instruction set computing

Cell's multicore archit., synergistic proc.. Gschwind, M., et al., March-April, 2006, pp. 10-24.

software-configurable processor architecture. Gonzalez, R.E., Sept.-Oct., 2006, pp. 42-51.

Redundancy

MRF reinforcer, a probabilistic element for space redundancy in nanoscale circuits. Nepal, K., et al., Sept.-Oct.,

2006, pp. 19-27.

opportunistic transient-fault detect.. Gomaa, M.A., et al., Jan.-Feb., 2006, pp. 92-99.

Research and development

ubiquitous clicks and complements. Greenstein, S., Sept.-Oct., 2006, pp. 7-8.

Risk analysis

legislating entrepreneurship, oxymoron. Greenstein, S., July-Aug., 2006, pp. 4, 86.

Routing

scalable perform., seastar interconnect, balanced bandwidth. Brightwell, R., et al., May-June, 2006, pp. 41-57.

S

Sampling methods

sampling method focusing on practicality. Gracia Perez, D., et al., Nov.-Dec., 2006, pp. 14-28.

Scheduling

modern processors, adaptive hist.-based memory schedulers. Hur, I., et al., Jan.-Feb., 2006, pp. 22-29.

Scheduling; cf. Processor scheduling

Sensors

using bulk built-in current sensors to detect soft errors. Neto, E.H., et al., Sept.-Oct., 2006, pp. 10-18.

Shared memory systems

coarse-grain coherence tracking, RegionScout and Region Coherence Arrays. Cantin, J.F., et al., Jan.-Feb., 2006, pp. 70-79.

HPC appls., designing, crossbar scheduler. Minkenberg, C., et al., May-June, 2006, pp. 58-71.

Simulation

computer architecture simulation and modeling (special section). July-Aug., 2006, pp. 5-82.

computer architecture simulation and modeling (special section intro.). Sherwood, T., et al., July-Aug., 2006, pp. 5-7.

sampling method focusing on practicality. Gracia Perez, D., et al., Nov.-Dec., 2006, pp. 14-28.

Software

court dismisses copyright champion's source code copyright suit. Stern, R., May-June, 2006, pp. 88-90.

Software debugging

deterministic replay debugging, BugNet, recording appl.-level execution. Narayanasamy, S., et al., Jan.-Feb., 2006, pp. 100-109.

Software development management

book review; *From Java to Ruby - Things Every Manager Should Know* (Tate, B.; 2006). Mateosian, R., July-Aug., 2006, pp. 84-85.

Software engineering

Xbox 360 syst. archit.. Andrews, J., et al., March-April, 2006, pp. 25-37.

Software engineering; cf. Software development manage-

ment; **Software tools**

Software management; cf. **Software development management**

Software testing

deterministic replay debugging, BugNet, recording appl.-level execution. Narayanasamy, S., et al., Jan.-Feb., 2006, pp. 100-109.

Software tools

deterministic replay debugging, BugNet, recording appl.-level execution. Narayanasamy, S., et al., Jan.-Feb., 2006, pp. 100-109.

Special issues and sections

computer architecture simulation and modeling (special section). July-Aug., 2006, pp. 5-82.

computer architecture simulation and modeling (special section intro.). Sherwood, T., et al., July-Aug., 2006, pp. 5-7.

high-performance interconnects (special section). May-June, 2006, pp. 7-84.

high-performance interconnects (special section intro.). Petriani, F., et al., May-June, 2006, pp. 7-9.

Micro's top picks from microarchitecture conferences (special section). Jan.-Feb., 2006, pp. 8-139.

Micro's top picks from microarchitecture conferences (special section intro.). Torrellas, J., Jan.-Feb., 2006, pp. 8-9.

SQL

book review; *The Art of SQL* (Faroult, S. and Robson, P.; 2006). Mateosian, R., May-June, 2006, pp. 86-87.

Statistical analysis; cf. **Sampling methods**

String matching

bit-split string scanning, intrusion detect., archits.. Tan, L., et al., Jan.-Feb., 2006, pp. 110-117.

Switches; cf. **Optical switches**

Synchronization

parameter variations on circuits and microarchitecture. Unsal, O.S., Nov.-Dec., 2006, pp. 30-39.

System documentation

book review; *Communication Design - Developing Web Site Documentation for Design and Planning* (Brown, D.M.; 2006). Mateosian, R., Sept.-Oct., 2006, p. 83.

book review; *CSS - The Missing Manual* (McFarland, S.; 2006). Mateosian, R., Sept.-Oct., 2006, p. 83.

Systems analysis; cf. **System documentation**

T

Telecommunication; cf. **Business communication**

Telecommunication congestion control

efficient, scalable congestion management for interconnection networks. Garcia, P.J., et al., Sept.-Oct., 2006, pp. 52-66.

Telecommunication control; cf. **Telecommunication congestion control**

Telecommunication networks; cf. **Broadband networks**

Time division multiplexing; cf. **Packet switching**

Transmission lines

leveraging wire properties at the microarchitecture level. Balasubramonian, R., et al., Nov.-Dec., 2006, pp. 40-52.

Transport protocols

M5 simulator, modeling networked systs.. Binkert, N.L., et al., July-Aug., 2006, pp. 52-60.

net. processor-based, content-aware switch. Li Zhao, et al., May-June, 2006, pp. 72-84.

U

Ubiquitous computing

ubiquitous clicks and complements. Greenstein, S., Sept.-Oct., 2006, pp. 7-8.

User interfaces

book review; *About Face 2.0: The Essentials of Interaction Design* (Cooper, A. and Riemann, R.; 2003). Mateosian, R., May-June, 2006, p. 87.

book review; *Interface Oriented Design* (Pugh, K.; 2006). Mateosian, R., Sept.-Oct., 2006, p. 82.

V

Virtual computers

computer syst. simul., SimFlex, stat. sampling. Wenisch, T.F., et al., July-Aug., 2006, pp. 18-31.

Virtual memories

unbounded transaction memory. Ananian, C.S., et al., Jan.-Feb., 2006, pp. 59-69.

W

Web design

book review; *CSS - The Missing Manual* (McFarland, S.; 2006). Mateosian, R., Sept.-Oct., 2006, p. 83.

book review; *.NET Internationalization - The Developer's Guide to Building Global Windows and Web Applications* (Smith-Ferrier, G.; 2007). Mateosian, R., Sept.-Oct., 2006, p. 83.

Web sites

book review; *Ajax Design Patterns - Creating Web 2.0 Sites with Programming and Usability Patterns* (Mahemoff, M.; 2006). Mateosian, R., Sept.-Oct., 2006, p. 82.

book review; *Communication Design - Developing Web Site Documentation for Design and Planning* (Brown, D.M.; 2006). Mateosian, R., Sept.-Oct., 2006, p. 83.

Web sites; cf. **Web design**

X

XML

book review; *Ajax Design Patterns - Creating Web 2.0 Sites with Programming and Usability Patterns* (Mahemoff, M.; 2006). Mateosian, R., Sept.-Oct., 2006, p. 82.

book review; *Secrets of RSS* (Holzner, S.; 2006). Mateosian, R., Sept.-Oct., 2006, p. 83.