RAM permits Multibus I use of 32-bit data

Zendex Corporation has begun production of a 4M-byte CMOS RAM board with a 16M-byte address space that permits 16-bit Multibus I architectures to use 32-bit data. The ZX-091 replaces 28 of the 32 reserved lines on the P2 connector (originally designed as a power fail bus). The new 28 bus lines include 16 data, four address, and eight control lines.

Address modes are available in linear addressing with 16K-byte granularity and four configuration modes or dynamic relocation with 64 64K-byte pages relocatable across the 16M-byte memory address space. Both modes have access speeds of 190 ns achieved by the use of 100-ns devices.

The ZX-091 contains 40-hour Ni-Cd battery backup and is recommended for industrial automations applications.

Pricing will be available from the company.

Reader Service Number 47

ISDN chips match data, microprocessor speeds

AT&T's ISDN Unite family of integrated circuits has been expanded with two "speedmatching" chips. The ICs detect the speed of incoming data and send that information to the microprocessor.

The T7112 Asynchronous Receiver/Transmitter Interface (ARTI), adjusts to the appropriate microprocessor speed and notifies the microprocessor of the change. ARTI provides standard UART functions with additional data buffering. The 8MHz device can be used as a single-channel, half-, or full-duplex interface for simultaneous data transmission and reception.

The LC1046 PCM Transceiver is an integrated digital signaling interface requiring two line interface transformers, three input termination resistors, and one capacitor. It provides full duplex transmit/receive interfacing at the DS-1 and DS-1C rates between a DSX and terminal equipment. Potential users of the LC1046 PCM are terminal equipment manufacturers, and PBX, T1 multiplexer, LAN, and T1 modem manufacturers.

AT&T's T7112 ARTI is available in 24-pin, plastic DIP packaging for $10 each per 1000; the LC1046 PCM Transceiver is priced at $26 for 1000-piece quantities.

Reader Service Number 48

Trapezoidal transceivers meet Futurebus standards

Texas Instruments is offering two trapezoidal bus-interface transceivers that have minimum rise and fall times of 1-3 ns and typical times of 3-6 ns. The SN75ALS056 octal transceiver consumes 75 milliamps of power, and the SN75ALS057 quad transceiver a maximum of 40 mA. Both octal and quad devices meet the electrical requirements of the proposed IEEE Standard P896.1 (Futurebus).

The new chips are fabricated in Impact technology with advanced oxide-isolated, low-power Schottky circuitry. By producing trapezoidal output waveforms that reduce crosstalk between adjacent channels, the devices can be used in distributed data-switching systems, telecommunications systems, and other applications with many closely coupled lines. The octal function can be used in address/data lines with common control signals. The quad device has independent driver- and receiver-output pins and a separate disable control for each driver; it is suitable for use in backplane bus arbitration lines.

Both chips operate from single 5V supplies. Characterized for operation from zero to 70 degrees Celsius, the transceivers are encased in plastic, 20-pin, small-outline and DIP packages. Suggested resale price in 100-piece quantities is $15 each.

Reader Service Number 49

Modem offers simultaneous 300/9600-bps channels

USRobotics states that more than 60 data communications programs will find their software compatible with the company's 9600-bps Courier HST modem. The list includes software for IBM PCs and PS/2's, Apple's Macintosh and II series, Unix/Xenix, and Novell Network.

Courier HST employs an asymmetrical, full-duplex modulation technique that provides simultaneous 9600-bps and 300-bps data channels. The high-speed channel uses the trellis-coded modulation described in CCITT recommendation V.32. Courier HST automatically assigns the high-speed channel direction based on dataflow demand.

Contact USRobotics for pricing.

Reader Service Number 50

Reader Interest Survey

Indicate your interest in this department by circling the appropriate number on the Reader Interest Card.

Low 180 Medium 181 High 182