Second-generation 32-bit FPC from Motorola

Motorola Microprocessor Products Group has announced a second-generation, 32-bit floating-point coprocessor, which is expected to offer two to four times the performance of the MC68881. The MC68882 enhanced FPC conforms to IEEE 754, the standard for binary floating-point arithmetic. It offers add, subtract, multiply, divide, and transcendental and non-transcendental functions.

The HCMOS VLSI device is designed to operate primarily as a coprocessor with the MC68020 and MC68030 32-bit MPUs through a transparent MC68000 coprocessor interface. In addition, the FPC can be used with M68000-family MPU devices and as a peripheral to non-M68000 processors.

The 16.67-MHz MC68882 FPC is enclosed in a 68-lead PGA package; it is expected to be available for sampling in April 1987 with production planned for August.

Motorola, Inc., Microprocessor Products Group, PO Box 3600, Austin, TX 78764; (512) 440-2839.

Reader Service Number 44

Intel boards combine Multibus, 80386 features

Four single-board computers from Intel Corporation use a 16-MHz, 80386 32-bit microprocessor and a dual-bus structure to provide high-end processing power for intricate applications. The iSBC 386/21, /22, /24, and /28 computers are supported by iRMS 286, Xenix, Unix System V, and proprietary operating system written for the 8086 or 80286 CPU.

The boards provide up to 8M bytes of 32-bit memory, which can be expanded to 16M bytes with add-on surface-mount modules. The increased memory provides users with direct CPU access to memory through a 64K-byte zero-wait-state cache memory without having to go out over the system bus. List prices are $4800 for the 386/21, $5970 for the /22, $8310 for the /24, and $12,990 for the /28.

Intel Corporation, 3065 Bowers Avenue, PO Box 58055, Santa Clara, CA 95052-8055; (503) 640-7399.

Reader Service Number 45

Three buses speed CMOS 32-bit multiplier

Advanced Micro Devices' Am29C323 is a 125-ns CMOS 32 × 32-bit parallel multiplier. The first member in a planned CMOS family of 32-bit microprogrammable building blocks, the Am29C323 uses less than one watt of power while operating at 8MHz.

The device's three buses contain two 32-bit input and one 32-bit output ports. It provides individual register feed-through controls, byte-parity checking on both input ports, and parity generation on the output port. Dual-precision registers on each data input port support multiprecision multiplication. A 64-bit product and a 3-bit overflow product permit the accumulation of values larger than the normal accumulator width.

During 1987 the company expects to introduce additional family products such as a 32-bit floating-point processor, 16-bit microprogram sequencer, 32-bit extended-function ALU, and 64 × 18 dual-access register file. The 125-ns Am29C323 is in production now; 100-ns and 80-ns versions are planned.

Intel Corporation, 901 Thompson Place, PO Box 3453, Sunnyvale, CA 94088; (408) 982-7448.

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