MicroNews

MicroNews features information of interest to professionals in the microcomputer/microprocessor industry. Send information for inclusion in MicroNews one month before cover date to Managing Editor, IEEE MICRO, 10662 Los Vaqueros Circle, Los Alamitos, CA 90720-2578.

SDIC conference to explore storage/ interface devices, systems architectures, networks

Computer systems designers, integrators, and specifiers, value-added resellers, value-added OEMs, and high-volume end users can look forward to the first meeting next February of the Systems Design and Integration Conference—an event in the planning stage for two years. Specifically designed to focus on practical solutions to the needs of these groups, SDIC will offer three days of technical sessions, tutorials, and exhibits of computer-related products.

Each day of the conference highlights one of three technologies: storage and interface devices, systems architectures, and networks. Phil Devin, senior analyst for Dataquest's Computer Storage Industry Service, will open the first day's session, speaking on that industry, its products, and its market trends. Planned technical sessions include software design and integration for 32-bit microprocessors, architectures for computer graphics (led by IEEE Micro editorial board member Richard Mateosian), and storage trends (led by IEEE Micro editorial board member Kenneth Majithia).

Walter J. Utz, Jr., Hewlett-Packard software engineering training manager, will open the next day's meeting with comments on the impact of RISC on computer design. Session topics include the impact of GaAs on systems design, 32-plus bus trends and choices, and systems design with 32-bit microprocessors.

SDIC's last day will begin with remarks from J. Edward Snyder, general manager, TRW Information Networks Division. Solving problems with LANs, multivendor systems integration, MAP: the key to an integrated factory, and fiber optics systems are some of the session topics.

Tutorials

SDIC tutorials are designed to help participants learn about the design and implementation of databases, motion control systems, AI/expert systems, and systems design methodologies and CAE/CAD tools, among other subjects. Tutorial instructors include consultant Herb Edelstein of Digital Consulting Associates; Stanford University associate professor Gio Wiederhold; researcher, author, engineer, and consultant Jacob Tal; and the director of University of Santa Clara's Center for Information Storage Technology, Al Hoagland.

Registration information

The Systems Design and Integration Conference is scheduled for February 10-12, 1987, at the Santa Clara Convention Center in San Jose, California. Wescon, the Los Angeles and San Francisco Bay Area councils of the IEEE, and the southern and northern California chapters of the ERA cosponsor the event.

General registration, which includes the opening session and exhibit areas on all three days, is $5. Preregistration fees for opening session, exhibit area, and each day's technical sessions are $80 per day. The seven attendance-limited tutorials cost $150 to $180.

More information on SDIC can be obtained from the conference producers, Electronic Conventions Management, at (213) 772-2965.

Editorial Board welcomes new members

Editor-in-Chief James J. Farrell III announced the acceptance of two new members to the IEEE Micro editorial board, Marlin H. Mickle and Yoichi Yano. Mickle assumes the duties of New Products editor beginning with this issue.

Marlin H. Mickle

Mickle received his BS, MS, and PhD degrees in electrical engineering from the University of Pittsburgh in 1961, 1963, and 1967.

Yoichi Yano

Yoichi Yano has been a microprocessor architecture designer at Microcomputer Products Division, NEC Corporation, since April 1980. During the past four years he participated in the architecture and system designs of the V60/V70 32-bit VLSI microprocessors. His current interests include VLSI processor architecture and highly parallel computing structures.

Reader Interest Survey

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