


For further reading


Michael L. Fuccio is a member of the Digital Signal Processor Design Group at AT&T Bell Laboratories, Holmdel, New Jersey. He was an architect of the first-generation Bellmac memory management unit and a member of the architectural team for the WE32100 microprocessor chip set. A member of Tau Beta Pi, Eta Kappa Nu, and the IEEE, he received the BSEE degree from the State University of New York at Stony Brook in 1980 and the MSEE degree from the Georgia Institute of Technology. He is pursuing graduate study at Rutgers University, where he is concerned with multiprocessor and parallel computing architectures for digital signal processing and computer graphics.

Questions about this article can be directed to Fuccio at AT&T Bell Laboratories, Room 2D-205, Crawfords Corner Rd., Holmdel, NJ 07733.

Benjamin Ng has been a member of the technical staff at AT&T Information Systems since 1981. He was a member of the architecture team for the WE32100 chip set and since has been an architect of a next-generation memory management unit. His areas of interest include computer architecture and computer graphics. A member of Tau Beta Pi, Eta Kappa Nu, the IEEE, and the ACM, he received a BS degree in electrical engineering from Cornell University in 1981 and an MS degree in electrical engineering from Columbia University in 1982.

Reader Interest Survey

Indicate your interest in this article by circling the appropriate number on the Reader Interest Card.

High 165 Medium 166 Low 167