Hz), start or stop conversion at a specific time, minimum allowable voltage step, and maximum current allowed. Periodic reports can be sent to the terminal, such as "the most exhausted source."

3. The COP watchdog adds further protection. It can be set so that it will be refreshed every 180 degrees, and in case of sudden software malfunction the system will be reset automatically.

4. Compensation of the turn-off/turn-on time difference of the optocouplers. The turn-off delay is usually a few microseconds longer than the turn-on delay. This difference is about the equivalent of the execution time of STA <ce> instruction, thus using the "break before make" technique: The difference in time is insignificant.

Figure 10 illustrates the possible design of the inverter based on the MC68HC11. Note that most of the functions are implemented on a single chip. A D/A must be added. The terminal is optional but recommended.

References


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