EDIFSCHM and EDIFPLOT are examples of software that use EDIF to create a macrocell array library. EDIFSCHM translates the designer's visual tool—the macrocell contained in the design manual—into EDIF. EDIFPLOT visually verifies the accuracy of the entered syntax. The most important feature of this application is that, though the user does not need to know EDIF, he or she can represent an entire macrocell array library in EDIF.

The future of EDIF

EDIF continues to evolve through the work of the various subcommittees. This work is based on previously recognized needs and the feedback from EDIF users. For example, to transmit procedurally defined data descriptions (such as behavioral models or procedurally defined layout data), several additional constructs are required. These constructs, which will resemble control flow statements in a procedural language, are currently under development by the Procedural Layout Subcommittee. Other areas under development include test program description, functional behavior, definition of logic model behavior, mask-level design rules, electrical design rules, and package description. These are intended for inclusion in a future release of EDIF.

An EDIF User Group has been formed to provide a forum for questions and comments. The first meeting was held June 27, 1985, following the Design Automation Conference. A digest of technical papers was distributed at this meeting. The group plans to meet again at ICCAD in November. For more information about the EDIF User Group, or for more information about obtaining copies of EDIF Version 1 0 0, contact: The EDIF User Group, Design Automation Department, Texas Instruments, PO Box 225474, MS3668, Dallas, TX 75265.

Bibliography

EDIF User Group, The First EDIF User Group Workshop Digest of Technical Papers, Texas Instruments, PO Box 225474, MS3668, Dallas, TX 75265, 1985.


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