ARTICLES

7 A Microcomputer-based Model Robot System with Pulse-Width Modulation Control
Richard E. Wainwright and Randy H. Moss
Constructed of relatively inexpensive but realistic and durable elements, this model is a practical tool for training in robot operation and for research in robotic control systems.

22 The Ampos Multiprocessor—A Computer System for Laboratory Use
Malcolm C. Harrison and Owen Smith
This highly modular processor management system has been designed to provide computational facilities for biomedical research laboratories. It uses a command language and interprocessor communications similar to those of the Unix system.

31 IEEE P1014—A Standard for the High-Performance VME Bus
Wayne Fischer
The popular VME bus allows high-speed interaction between 8-, 16-, and 32-bit microprocessors. This proposed standard outlines a flexible, asynchronous design capable of accommodating a full 32-bit address and data path.

42 Cuenet—A Distributed Computing Facility
T. Radhakrishnan and C. P. Grossner
This flexible system, supporting any interconnection topology specified by a programmer, allows system analysts to experiment with alternate program decomposition strategies.

53 Events and Interrupts in Tightly Coupled Multiprocessors
Hubert Kirrmann
In a decentralized multiprocessor, events must be stored, queued, and acknowledged. This can be accomplished by implementing a special event receiver called a synapse and by utilizing several forms of acknowledge.

DEPARTMENTS

3 Microview: Congress enacts its high-tech agenda
67 Micronews
68 Microreview: The AT&T Personal Computer 6300
72 Microstandards: The cost of inadequate microcomputer standards
74 Microlaw: The Semiconductor Chip Protection Act
76 New Products
83 Product Summary
85 Access
88 Advertiser/Product Index
91 Professional Calendar

Reader service cards, p. 89.
Change of address, p. 88.